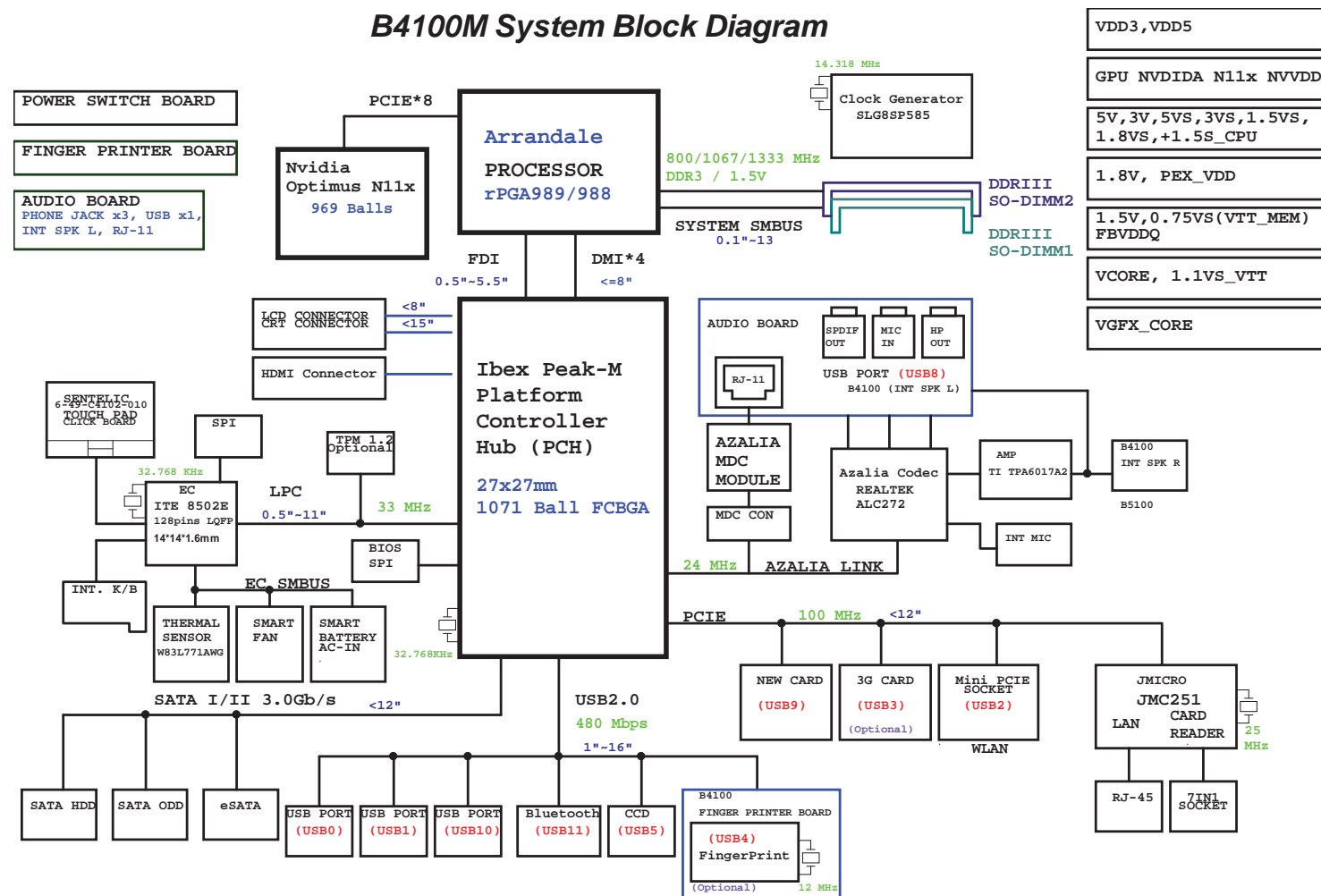


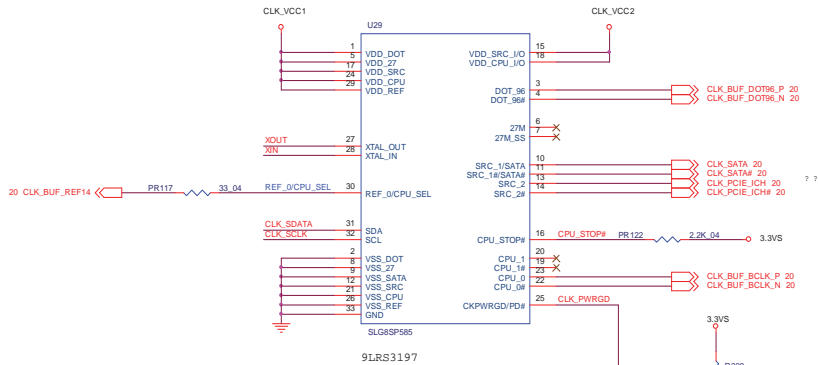
## System Block Diagram



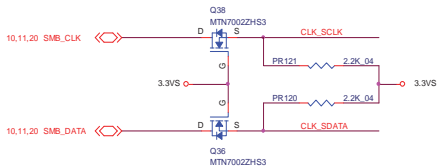
Sheet 1 of 49  
System Block  
Diagram

Clock Generator

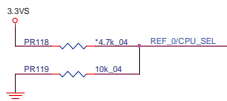
CLOCK GENERATOR



SMBus

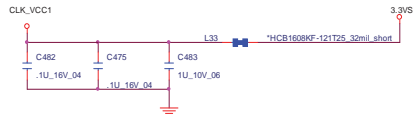


CPU\_SEL\_During CK\_PEWGD Latch Pin1



PIN_30	CPU_0	CPU_1
0(default)	133MHz	133MHz
1(0.7V-1.5V)	100MHz	100MHz

CLKGEN POWER



0.1uF near the every power pin



0.1uF near the every power pin

EMI

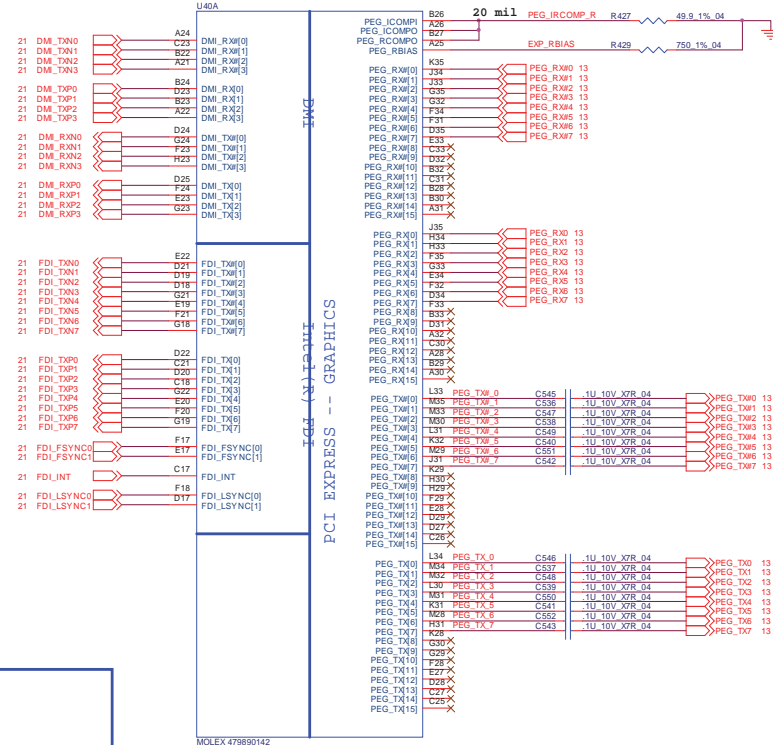
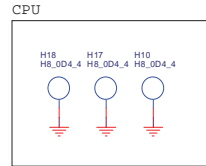


Sheet 2 of 49  
Clock Generator

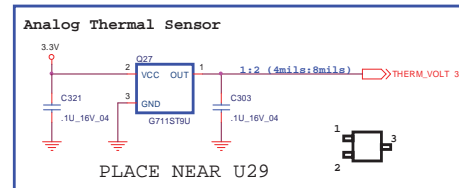
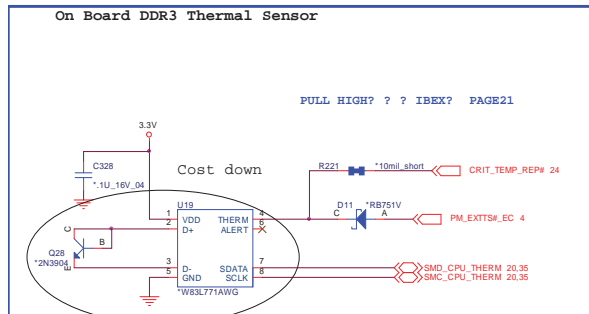
## Schematic Diagrams

# CPU 1/7 (DMI, PEG, FDI)

PROCESSOR 1/7 ( DMI,PEG,FDI )

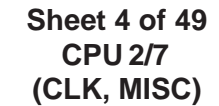


It applies to Auburndale and Clarkfield discrete graphic designs.  
If discrete graphic chip is used for Auburndale, Vaux (GFX core) rail can be connected to GND if motherboard only supports discrete graphics and also in a common motherboard design if GFX VR is not stuffed. On the other hand, if the VR is stuffed, Vaux can be left floating in a common motherboard design (GFX VR keeps Vaux from floating).  
In addition, FDI\_RXN[7:0] and FDI\_RXP[7:0] can be left floating on the PCB.  
FDI\_TX[7:0] and FDI\_TXN[7:0] can be left floating on the Auburndale.  
The GFX\_HMGR, FDI\_FSYN0[0], FDI\_FSYN0[1], FDI\_LSYN0[0], FDI\_LSYN0[1], and FDI\_INT signals should be tied to GND (through 1K ? resistors) in the common motherboard design case. Please note that if these signals are left floating, there are no functional impacts but a small amount of power (~15 mW) maybe wasted. Vaux sense and Vaux sense on Auburndale can be left as no connect.  
DPLL\_REF\_SCLK and DPLL\_REF\_SCLK# can be connected to GND on Auburndale directly if motherboard only supports discrete graphics. In a common motherboard design, these pins are driven via PCM (even if Graphics is disabled by BIOS) thus no external termination is required.



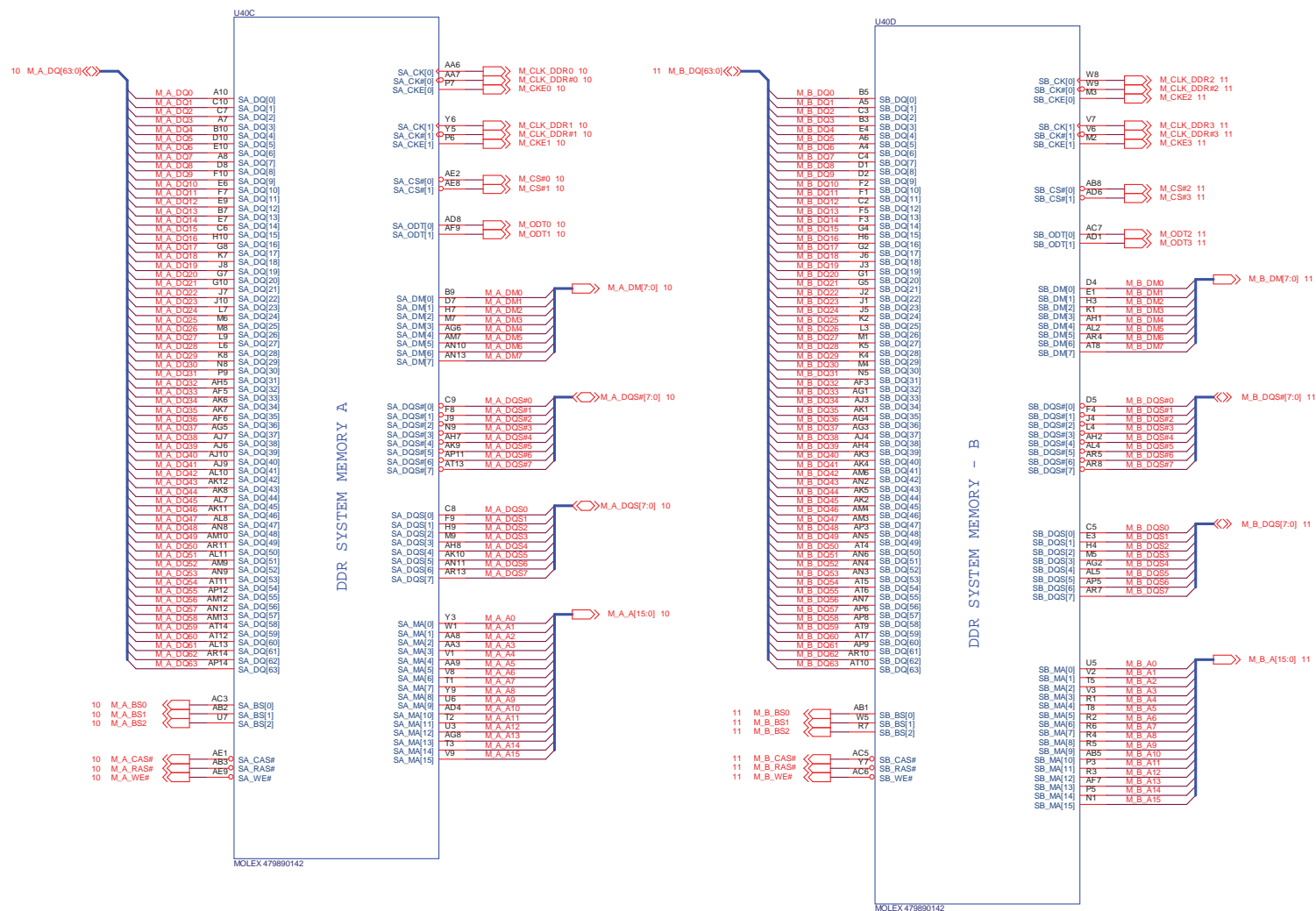
4,12,13,17,19,20,21,23,24,26,28,29,31,32,33,36,38,39,40,43 3.3V

PROCESSOR 2/7 ( CLK,MISC,JTAG )



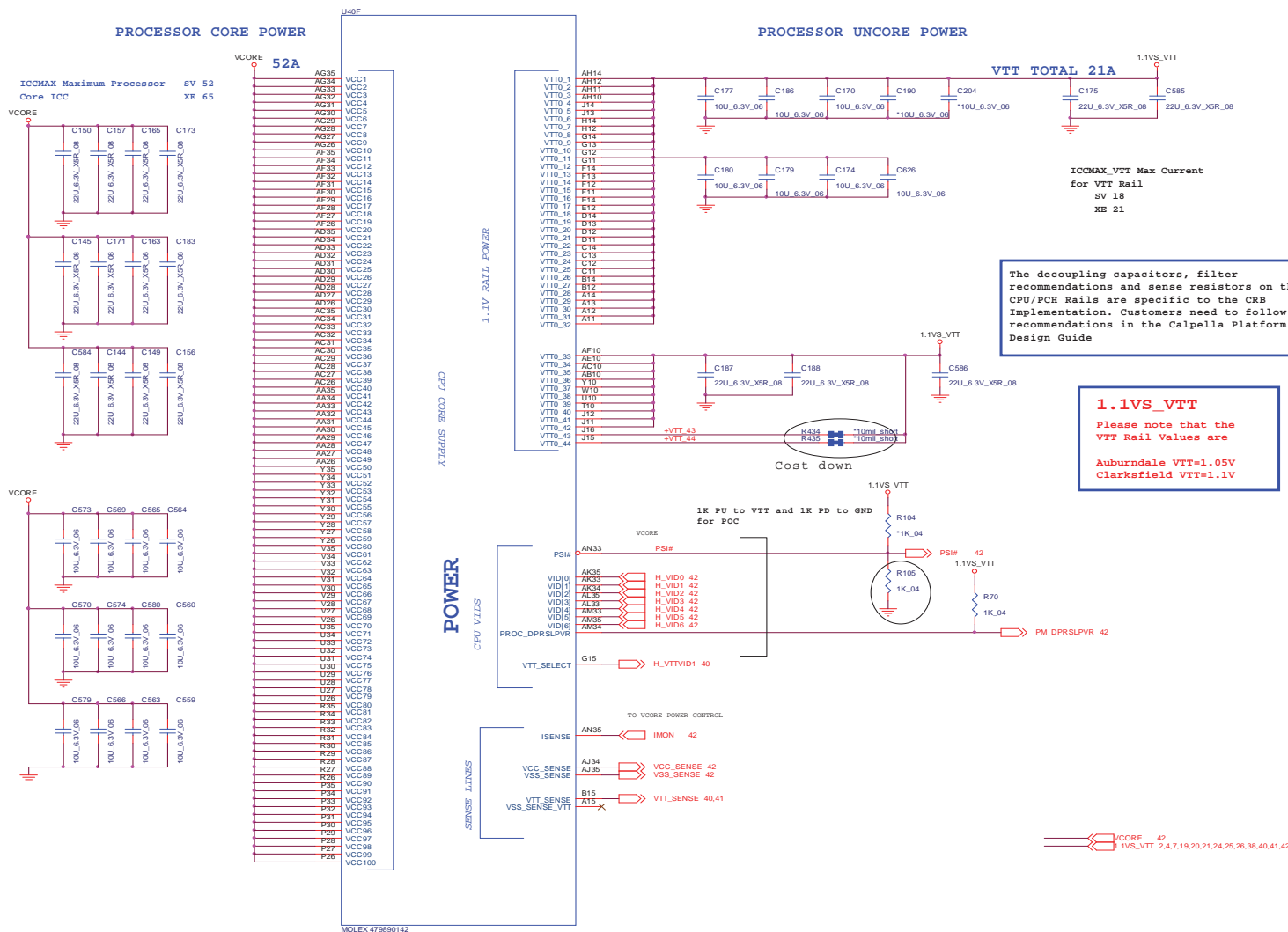
## CPU 3/7 (DDR3)

## PROCESSOR 3/7 ( DDR3 )



# CPU 4/7 (Power)

## PROCESSOR 4/7 ( POWER )



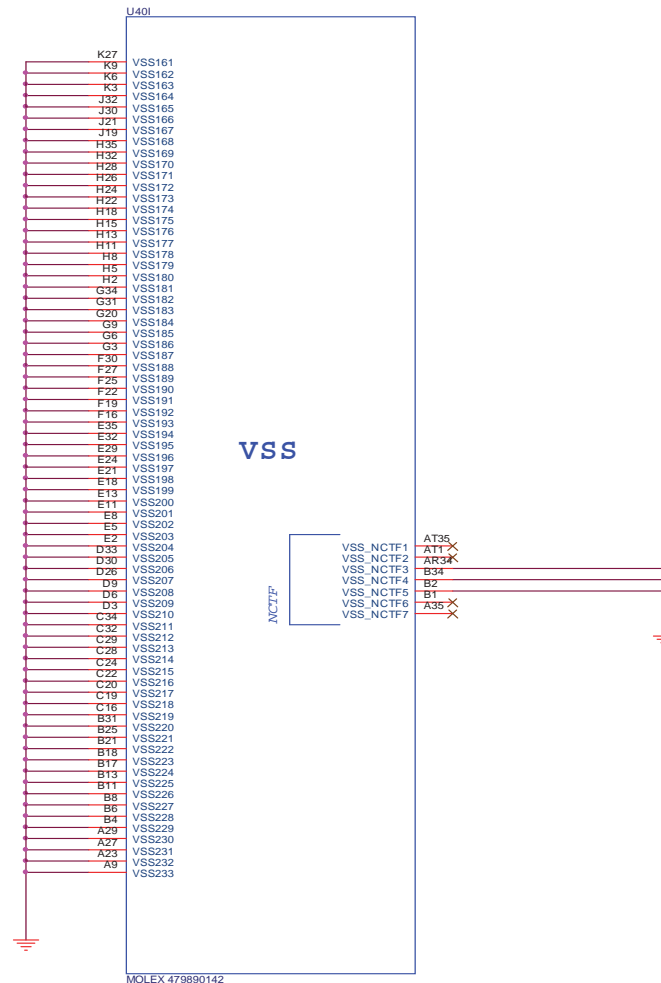
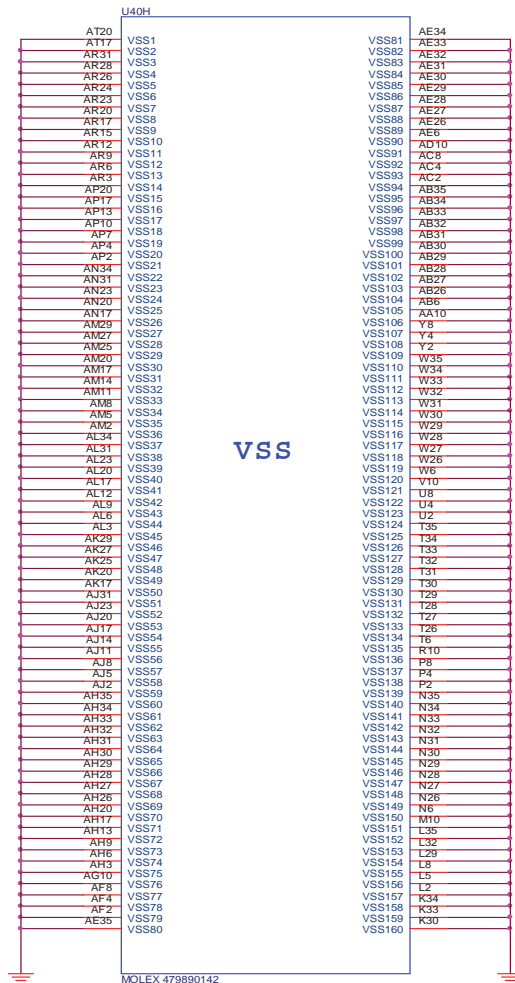
Sheet 6 of 49  
CPU 4/7  
(Power)

PROCESSOR 5/7 ( GRAPHICS POWER )



## CPU 6/7 (GND)

### PROCESSOR 6 / 7 ( GND )



Sheet 8 of 49  
CPU 6/7 (GND)



# CPU 7/7 (RESERVED)

## PROCESSOR 7/7 ( RESERVED )

PCI-Express Configuration Select	
CFG0	1 : Single PEG 0 : Bifurcation enable

CFG0 R106 \*3.01k 04

CFG3 - PCI-Express Static Lane Reversal	
CFG3	1 : Normal Operation 0 : Lane Numbers Reversed 15 -> 0, 14 -> 1, ...

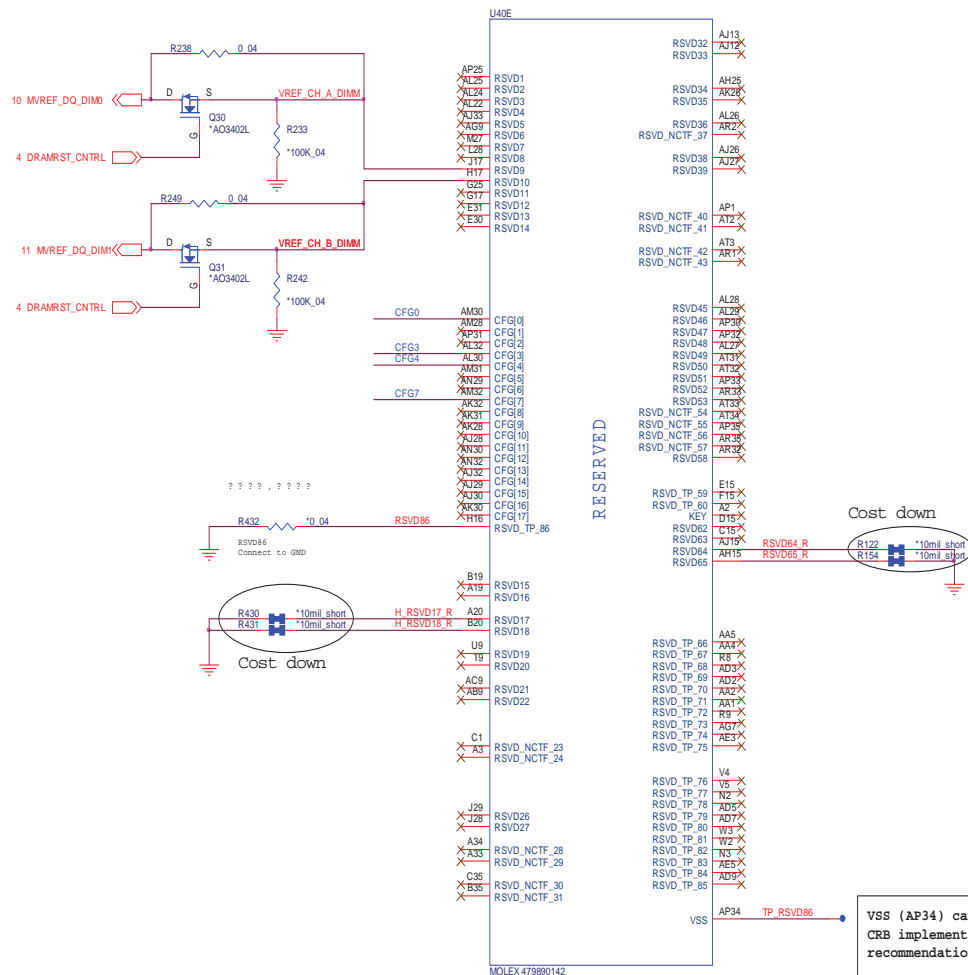
CFG3 R61 \*3.01k 04

CFG4 - Display Port Presence	
CFG4	1 : Disabled; No physical Display Port attached to Embedded Display Port 0 : Enabled; An external Display Port device is connected to the Embedded display Port

CFG4 R102 \*3.01k 04

CFG7 R62 \*3.01k 04

CFG7  
Clarkfield (only for early samples pre-ES1) - Connect to GND with 3.01K Ohm/5% resistor

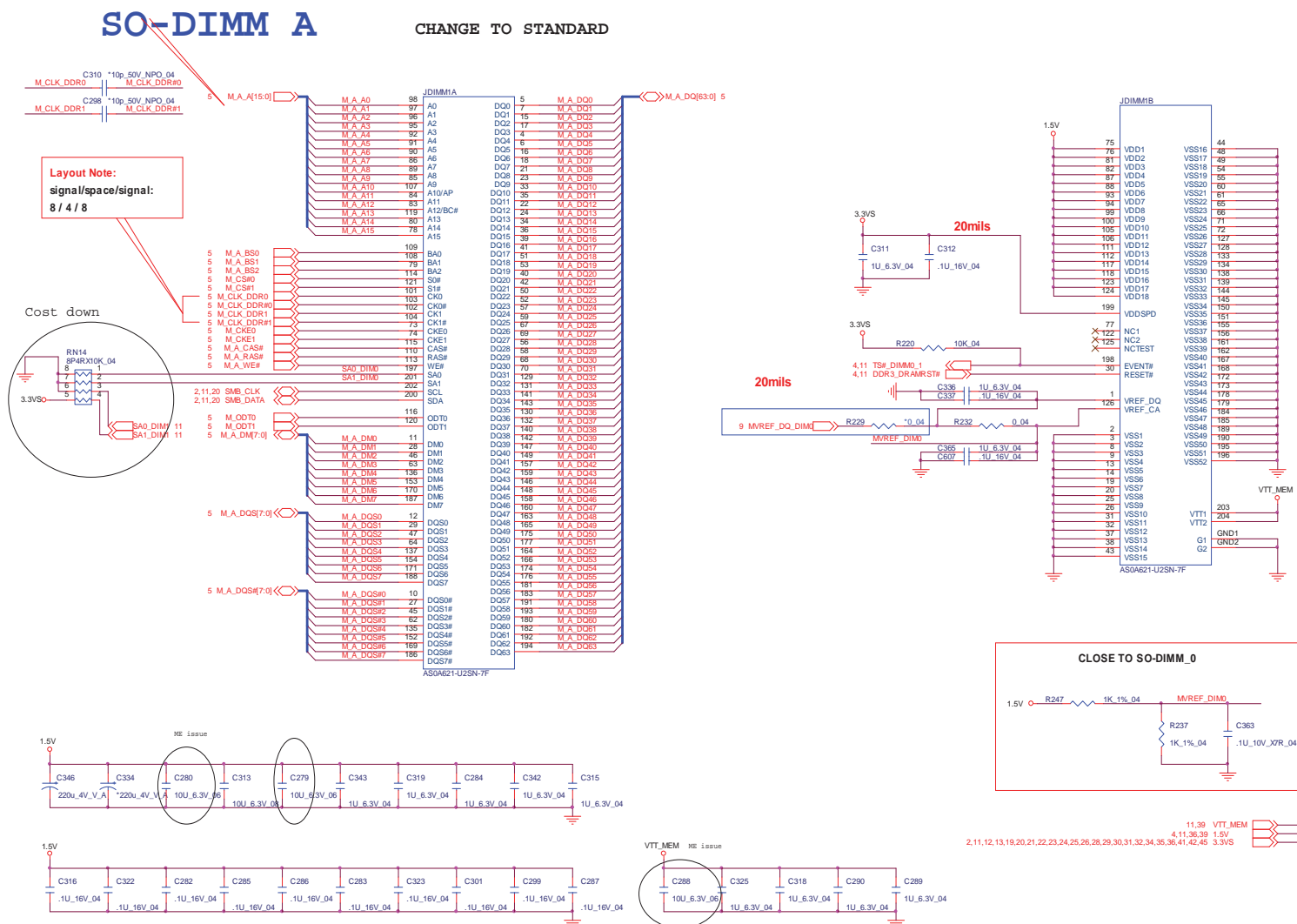


Cost down

Cost down

VSS (AP34) can be left NC i CRB implementation ; EDS/DG recommendation to GND

## DDR3 SO-DIMM\_0

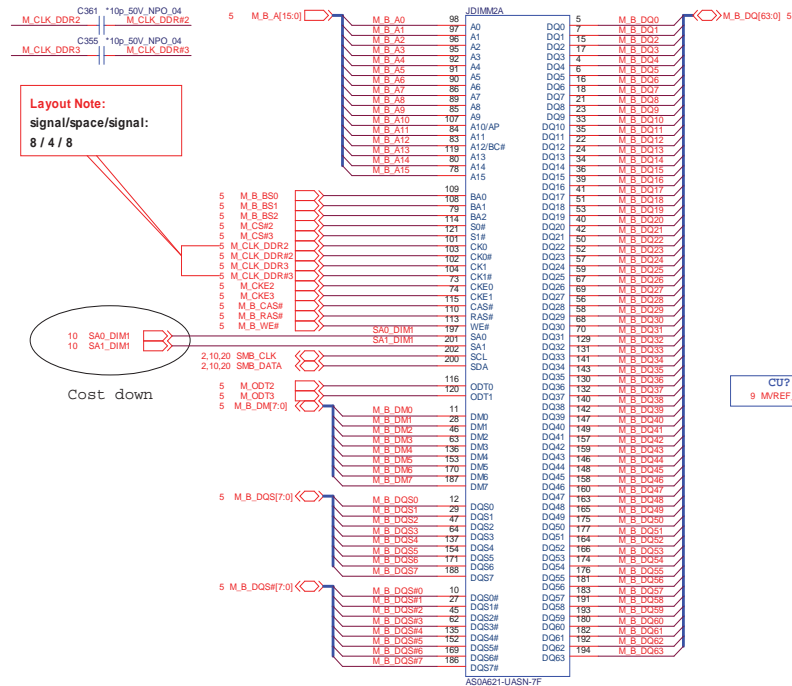


## Schematic Diagrams

# DDR3 SO-DIMM\_1

## SO-DIMM B

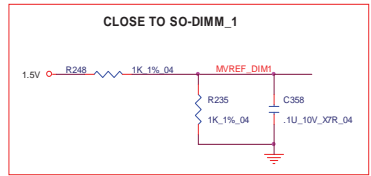
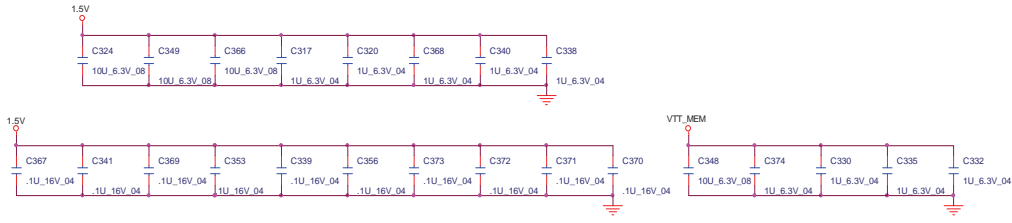
CHANGE TO STANDARD



Layout Note:  
signal/space/signal:  
8 / 4 / 8

Cost down  
10 SA0\_DIM1  
10 SAT\_DIM1

Layout Note:  
SO-DIMM\_1 is placed farther from the GMCH than SO-DIMM\_0

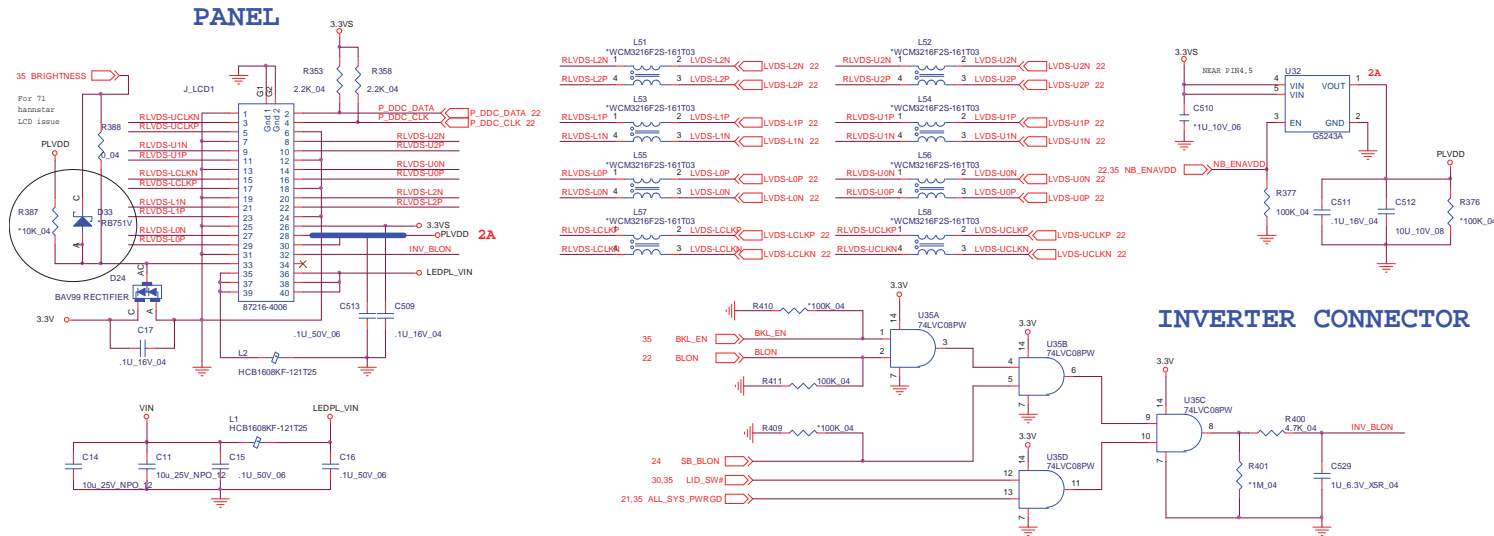


10,39 VTT\_MEM  
4,10,36,39 1.5V  
2,10,12,13,19,20,21,22,23,24,25,26,28,29,30,31,32,34,35,36,41,42,45 3.3VS

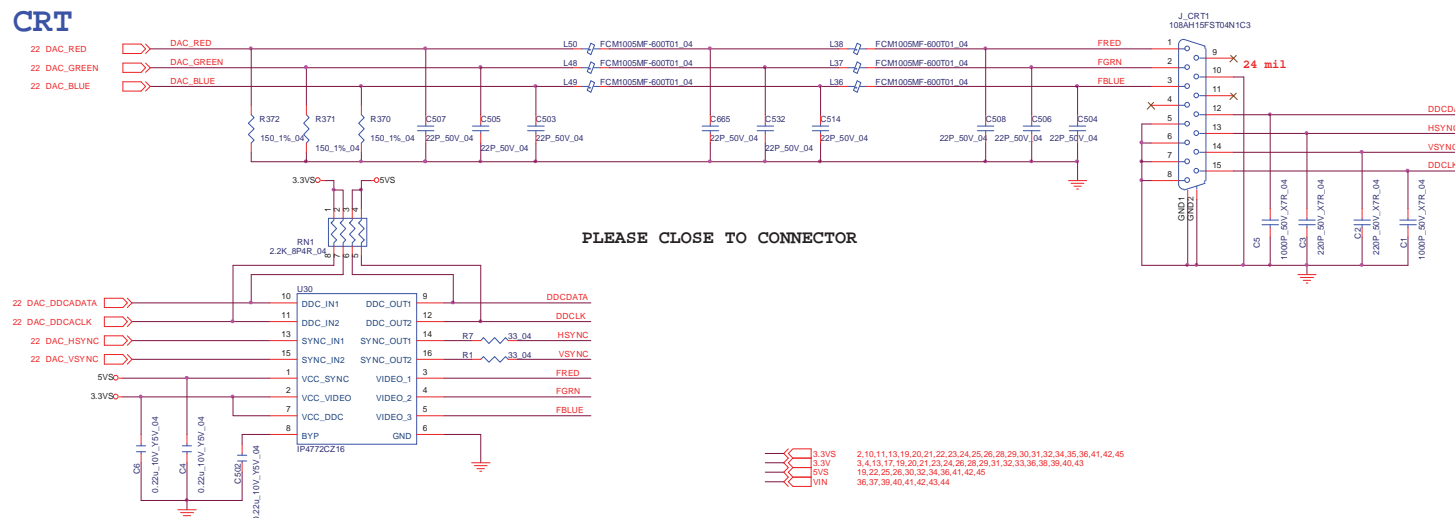
B.Schematic Diagrams

Sheet 11 of 49  
DDR3 SO-DIMM\_1

## Panel, Inverter, CRT

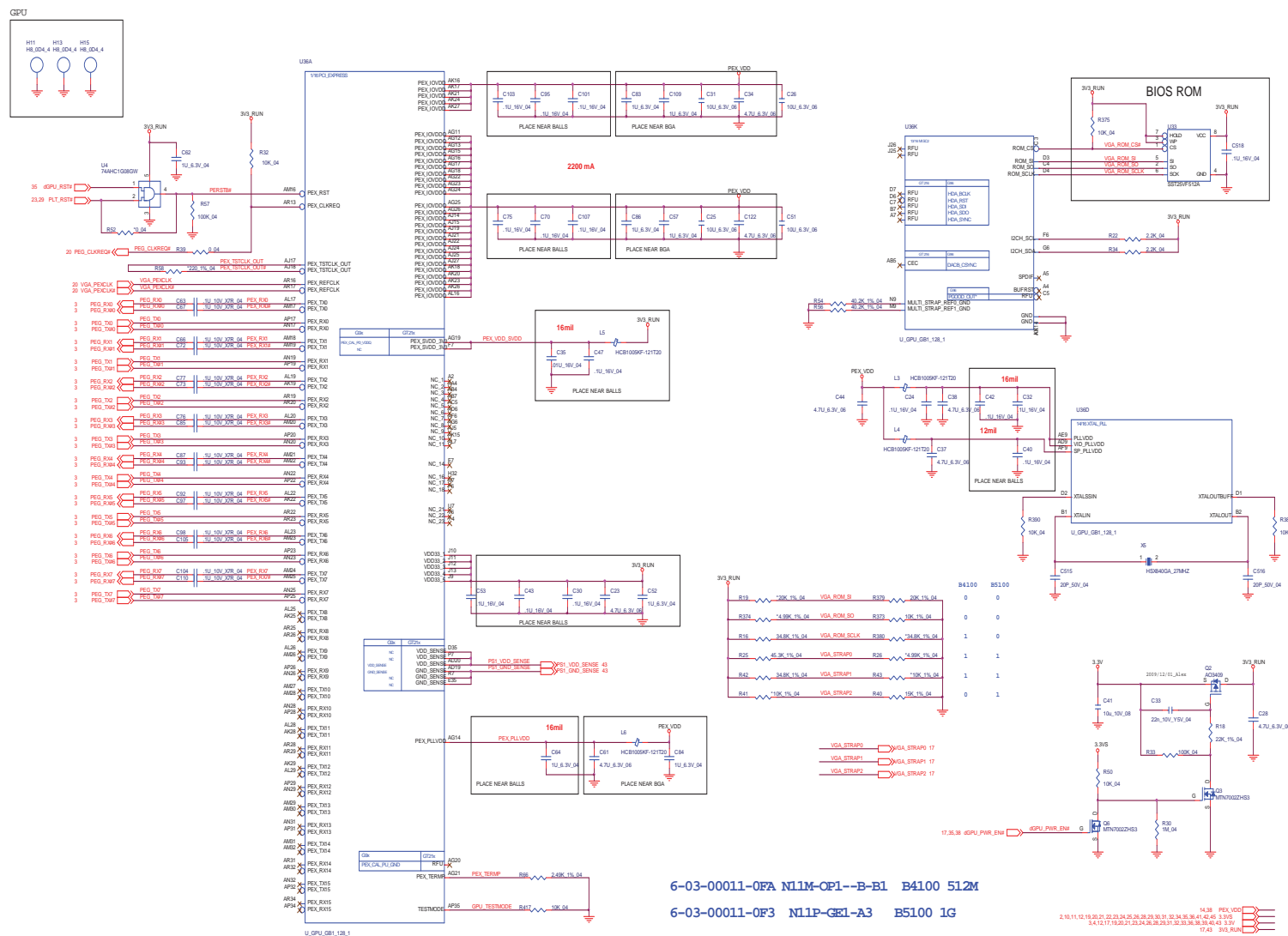


## CRT



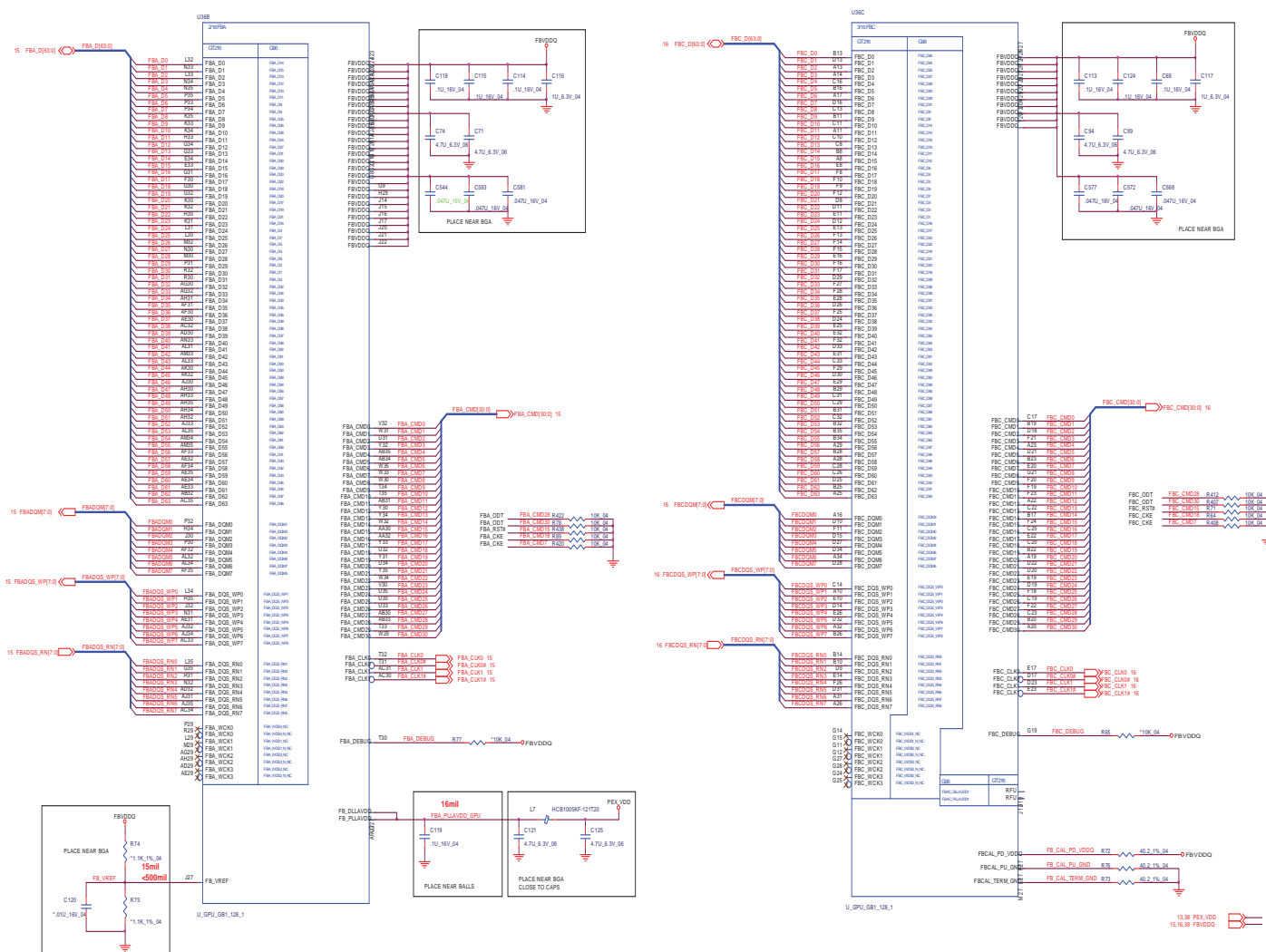
## B.Schematic Diagrams

**Sheet 13 of 49**  
**VGA PCI-E**  
**Interface**



## VGA Frame Buffer Interface

## Frame Buffer Interface



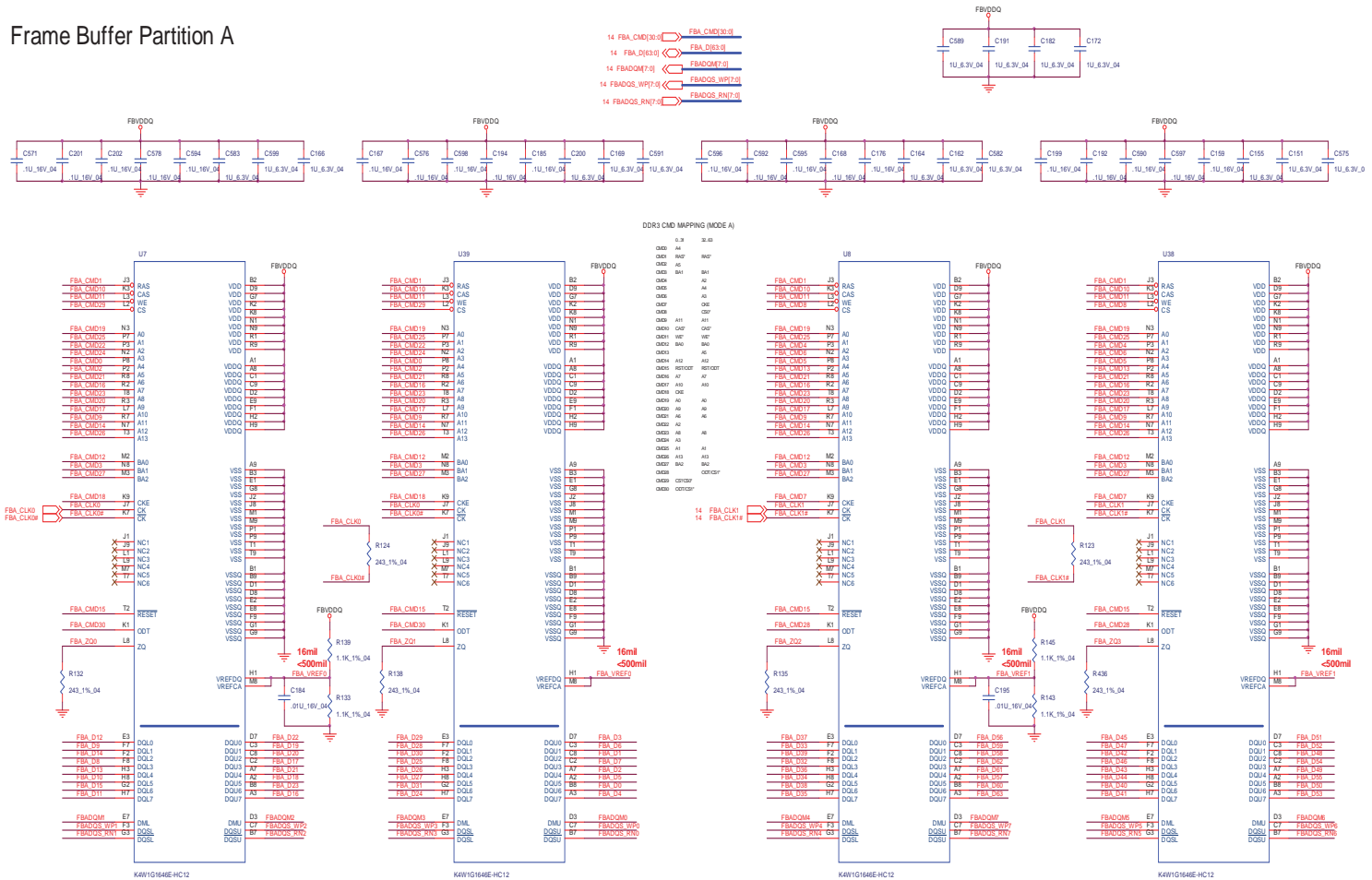
Sheet 14 of 49  
VGA Frame Buffer  
Interface

## B.Schematic Diagrams

## Schematic Diagrams

# VGA Frame Buffer A

### Frame Buffer Partition A



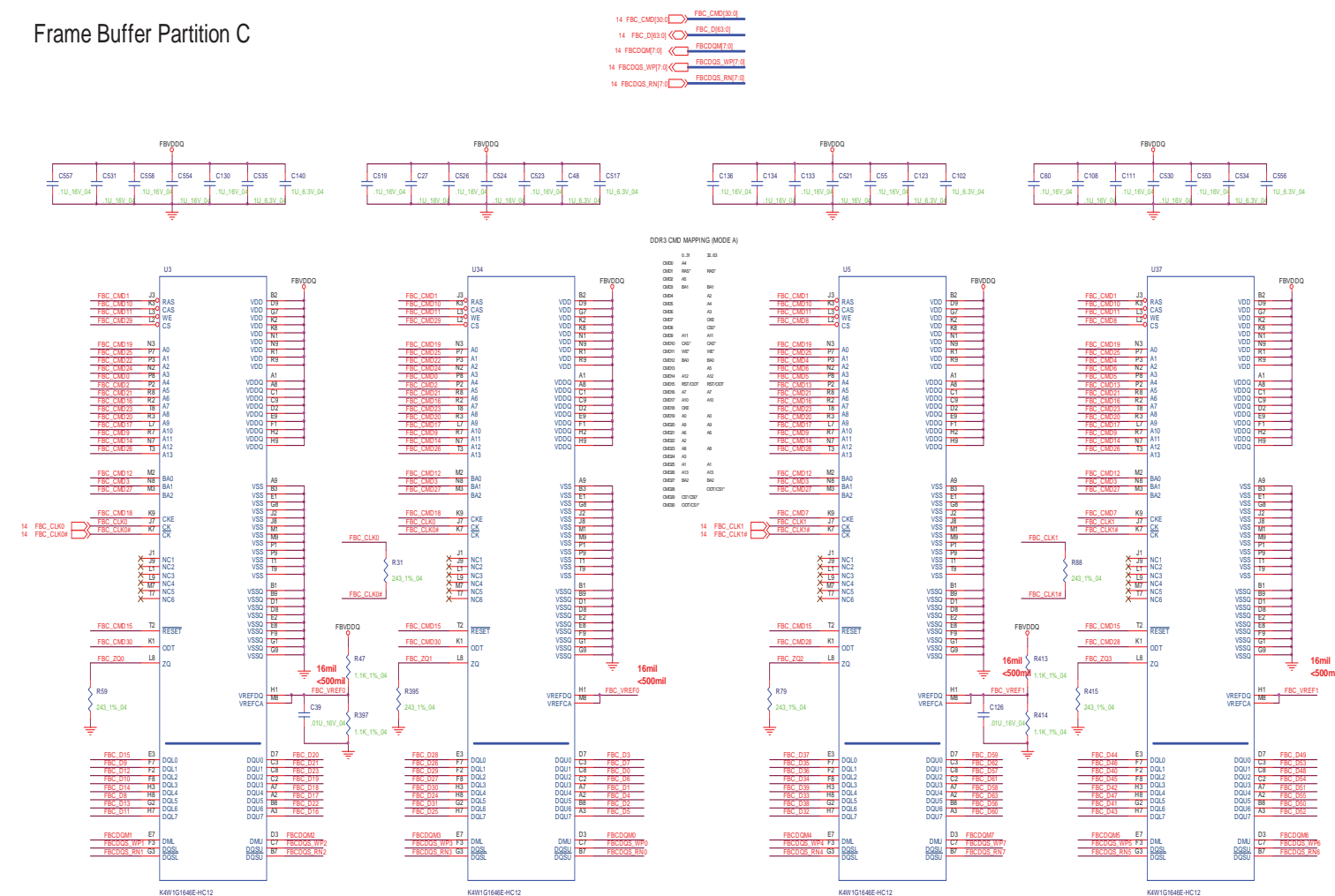
14,16,38 FBVDDQ

B.Schematic Diagrams

Sheet 15 of 49  
VGA Frame Buffer  
A

# VGA Frame Buffer C

## Frame Buffer Partition C

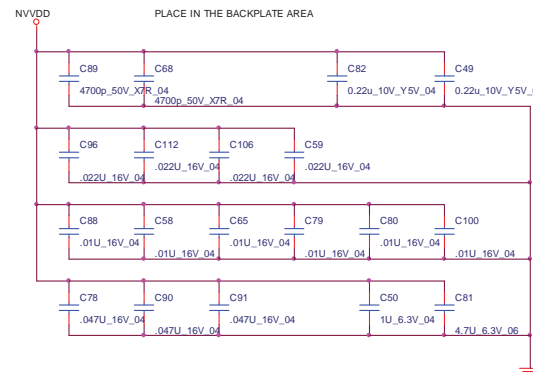
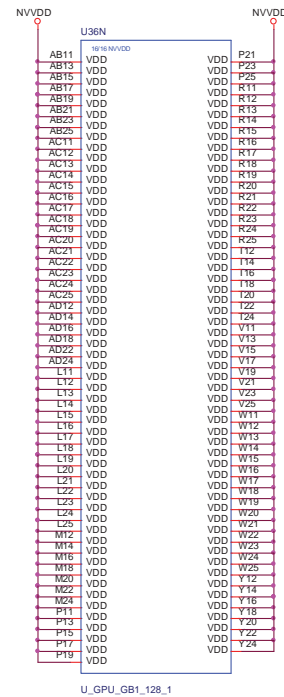
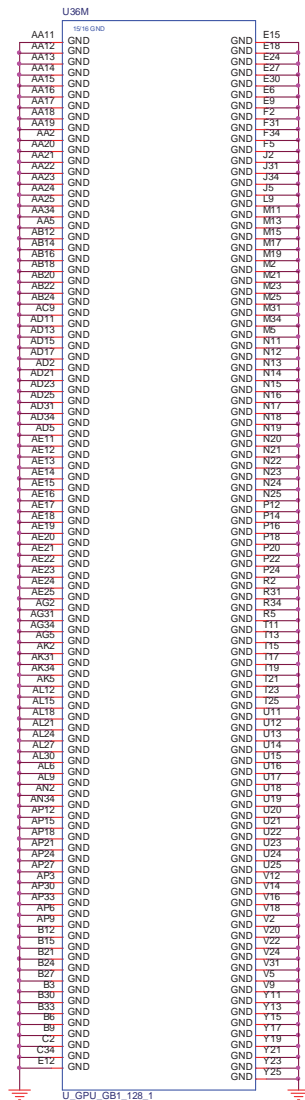


Unstuff for B4100



## B.Schematic Diagrams

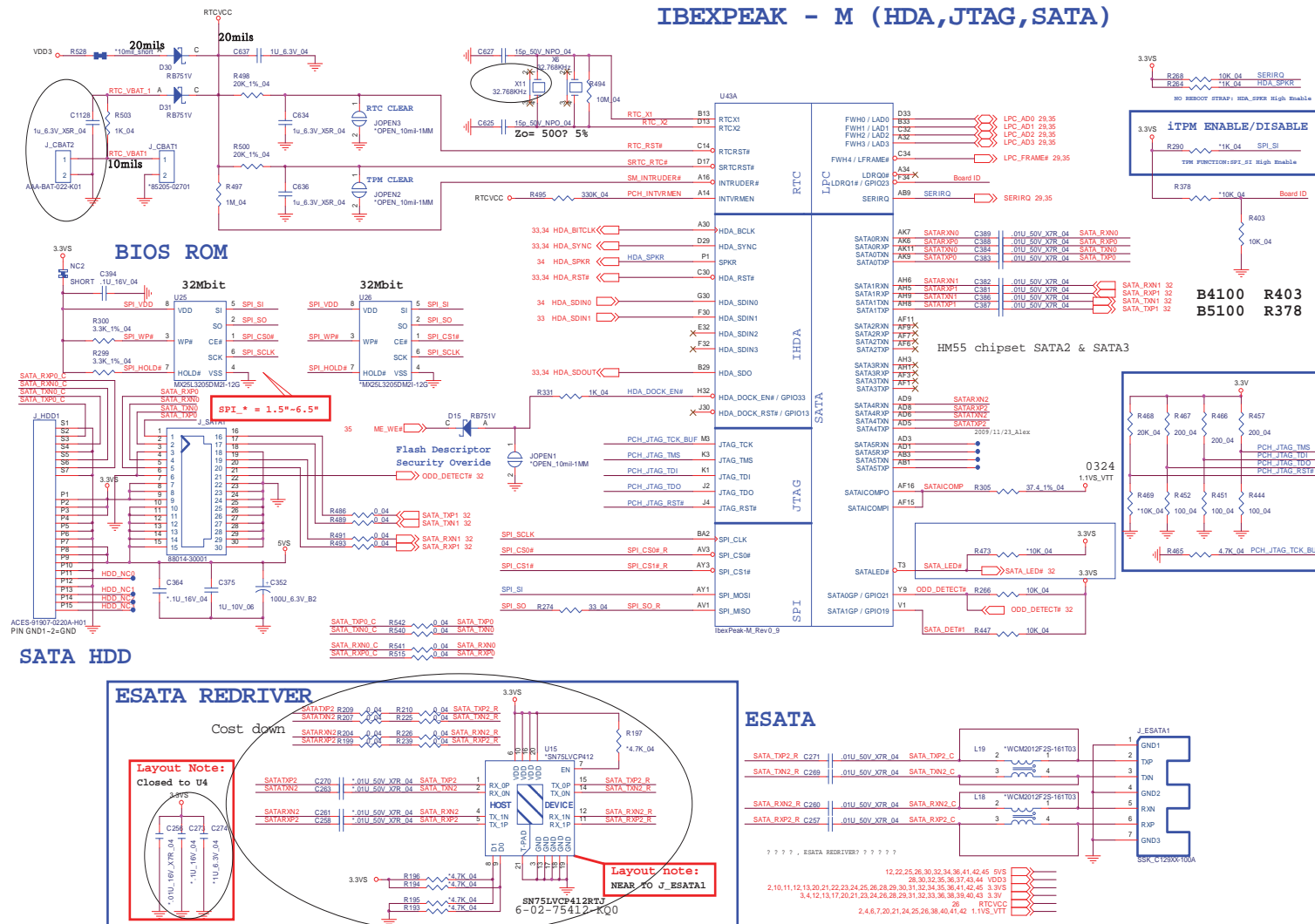
## VGA NVVDD Cdecoupling



**Sheet 18 of 49**  
**VGA NVVDD**  
**Cecoupling**

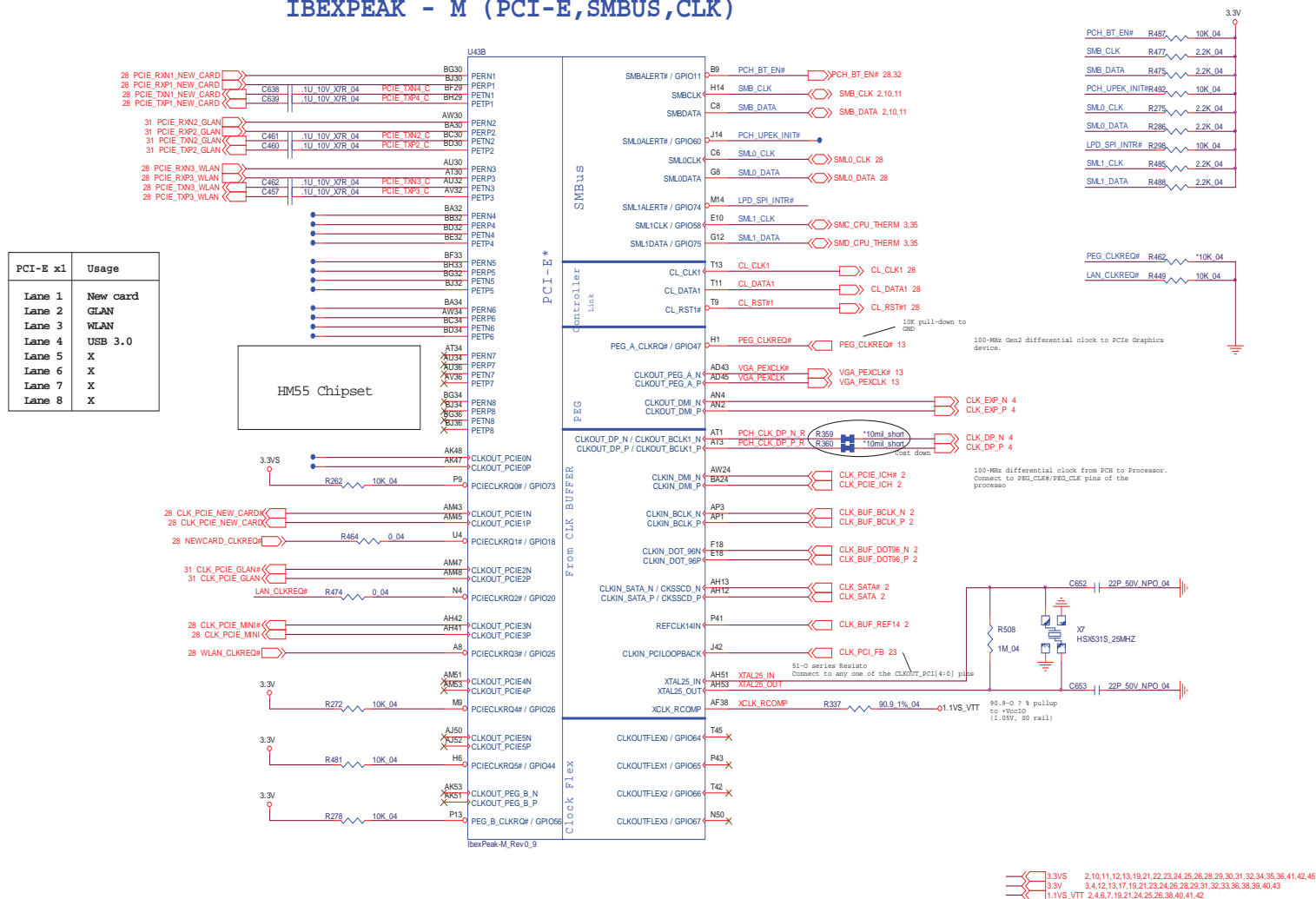
# IBEXPEAK- M 1/9

## IBEXPEAK - M (HDA,JTAG,SATA)



# IBEXPEAK - M 2/9

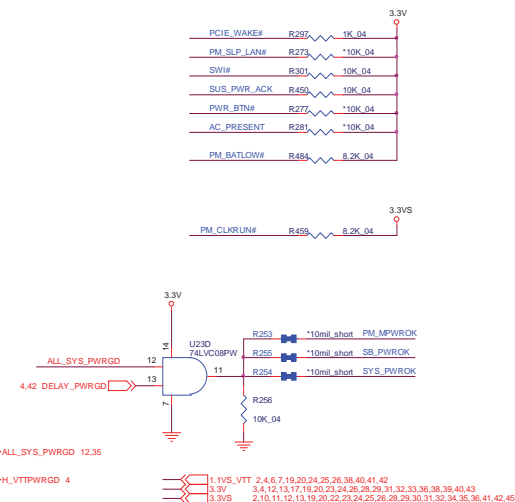
## IBEXPEAK - M (PCI-E,SMBUS,CLK)



Sheet 20 of 49  
IBEXPEAK - M 2/9

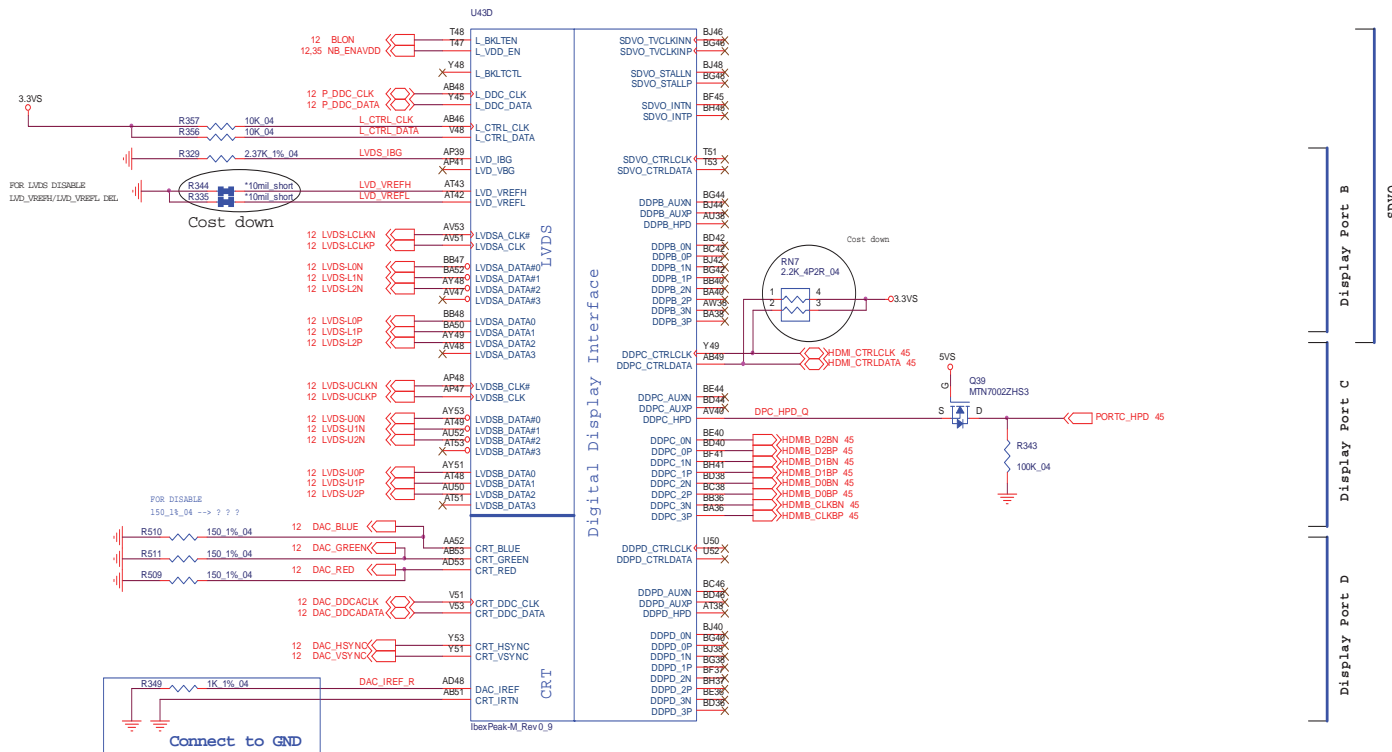
## IBEXPEAK - M (DMI,FDI,GPIO)

## B.Schematic Diagrams

[illegible]

# IBEXPEAK - M 4/9

## IBEXPEAK - M (LVDS,DDI)



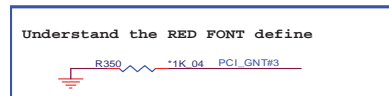
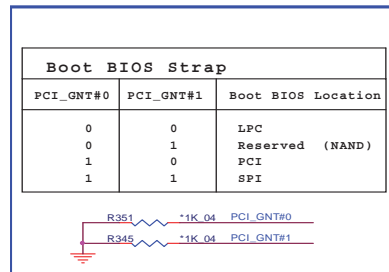
Sheet 22 of 49  
IBEXPEAK - M 4/9

5VS  
3.3VS  
12,19,25,26,30,32,34,36,41,42,45  
2,10,11,12,13,19,20,21,23,24,25,26,28,29,30,31,32,34,35,36,41,42,45

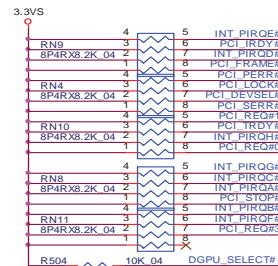
## Schematic Diagrams

# IBEXPEAK - M 5/9

## IBEXPEAK - M (PCI,USB,NVRAM)

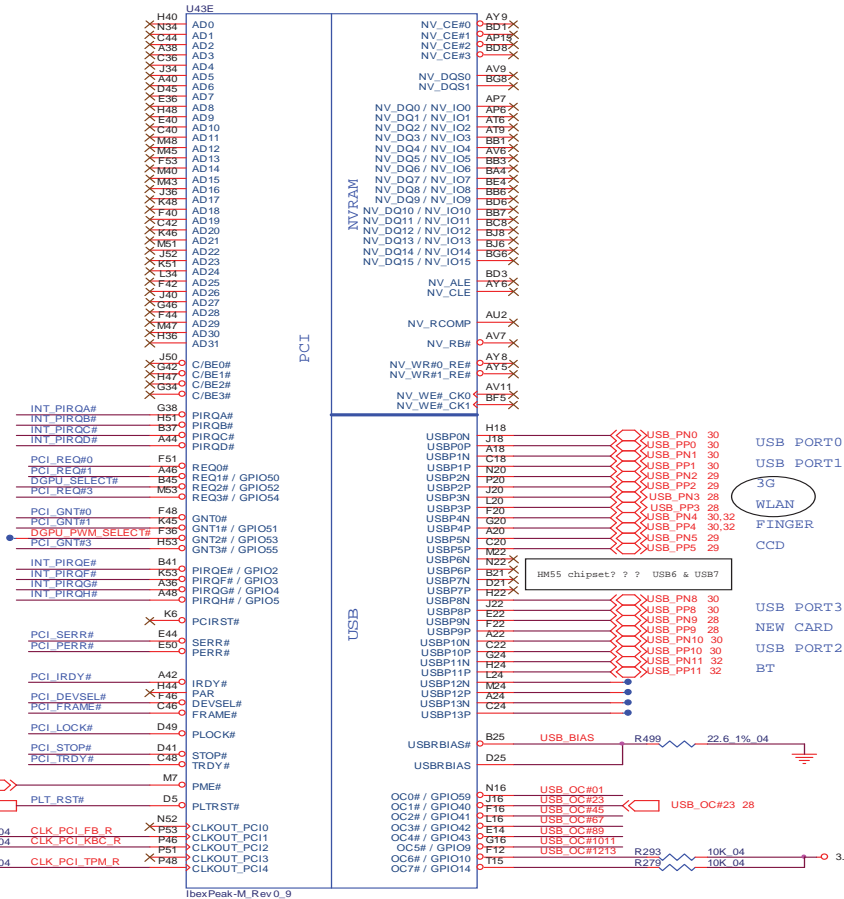
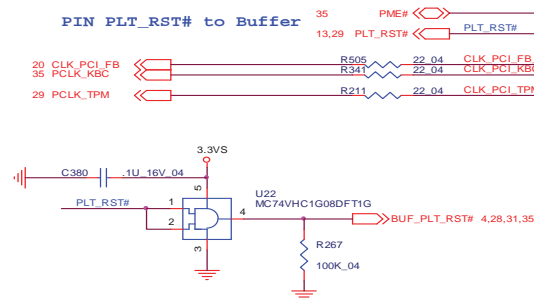


Sheet 23 of 49  
IBEXPEAK - M 5/9



BACKLIGHT CONTROL FROM IGPU/DGPU

PIN PLT\_RST# to Buffer

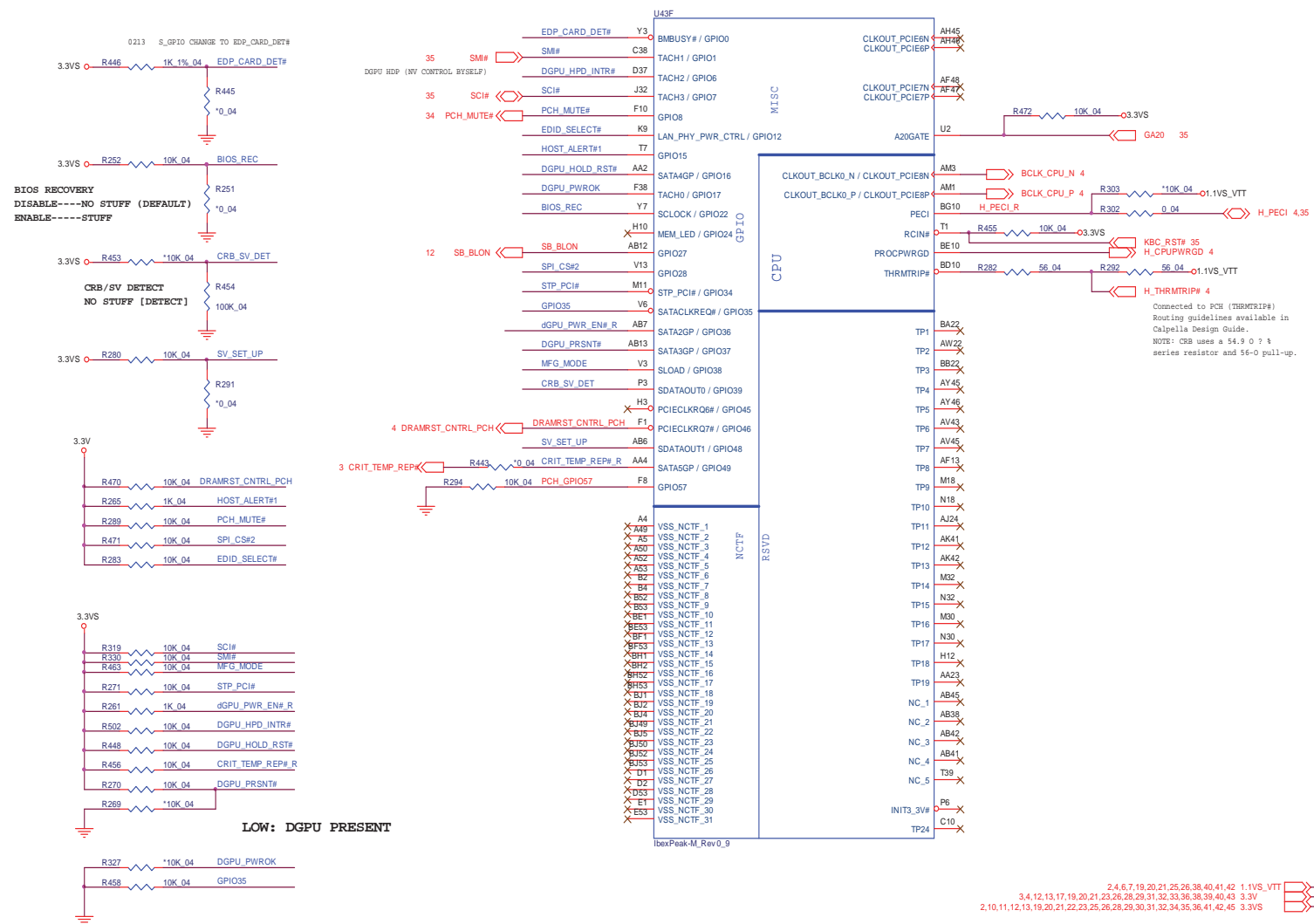


2,10,11,12,13,19,20,21,22,24,25,26,28,29,30,31,32,34,35,36,41,42,45 3.3VS  
3,4,12,13,17,19,20,21,24,26,28,29,31,32,33,36,38,39,40,43 3.3V



# IBEXPEAK - M 6/9

## IBEXPEAK - M (GPIO,VSS\_NCTF,RSVD)



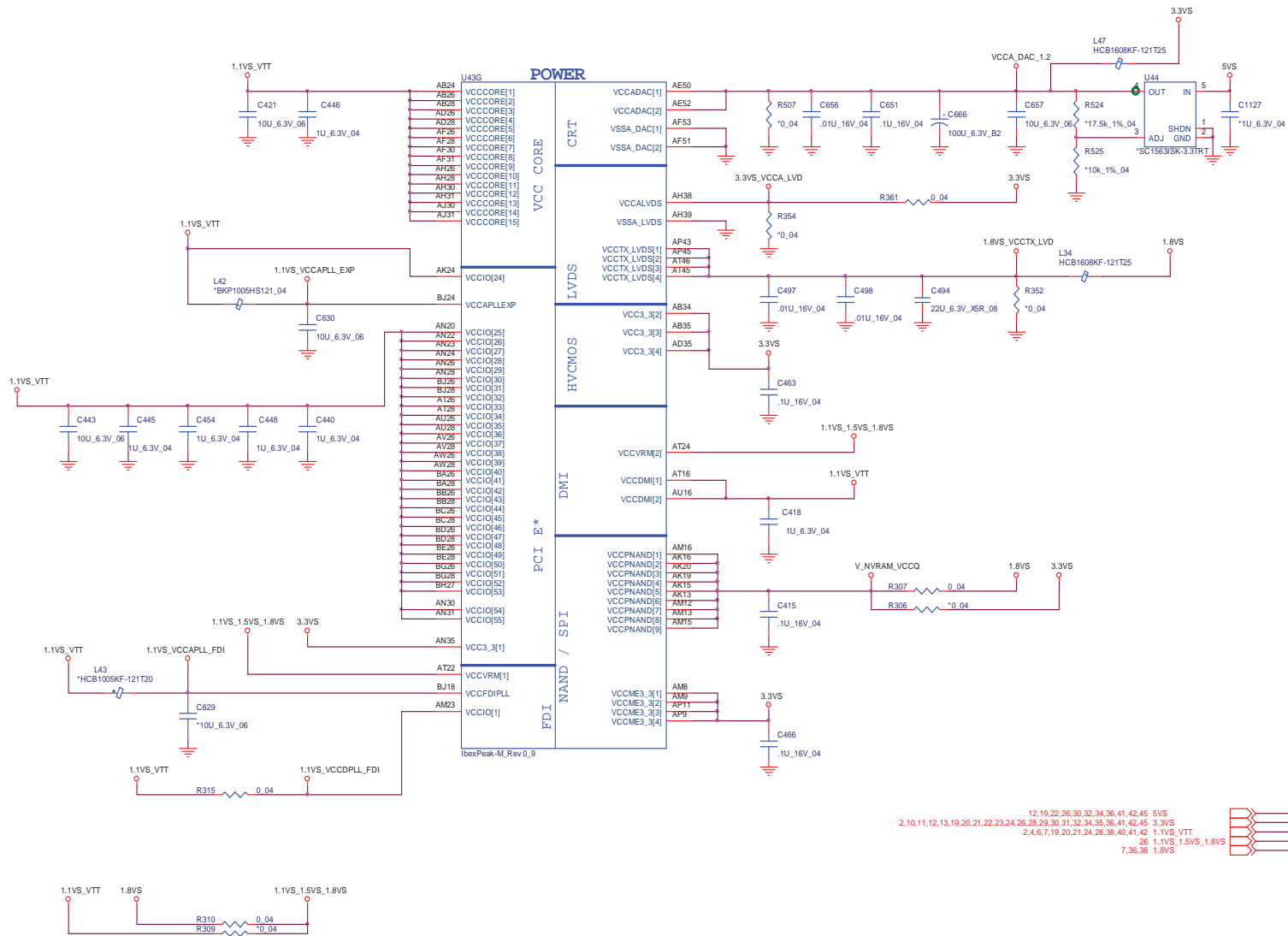
Sheet 24 of 49  
IBEXPEAK - M 6/9



# IBEXPEAK - M 7/9

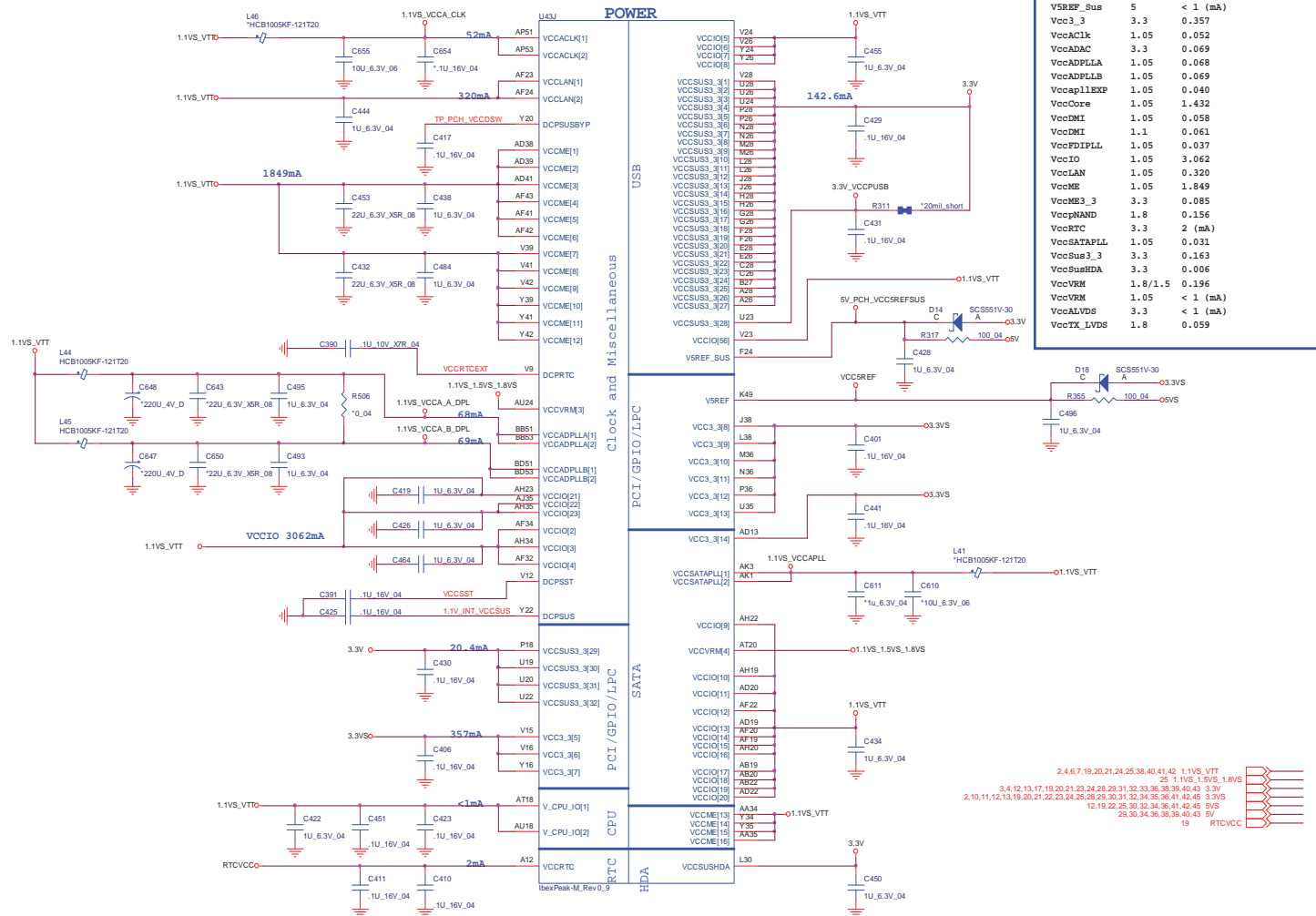
## IBEXPEAK - M (POWER)

Sheet 25 of 49  
IBEXPEAK - M 7/9



## IBEXPEAK - M 8/9

## IBEXPEAK - M (POWER)



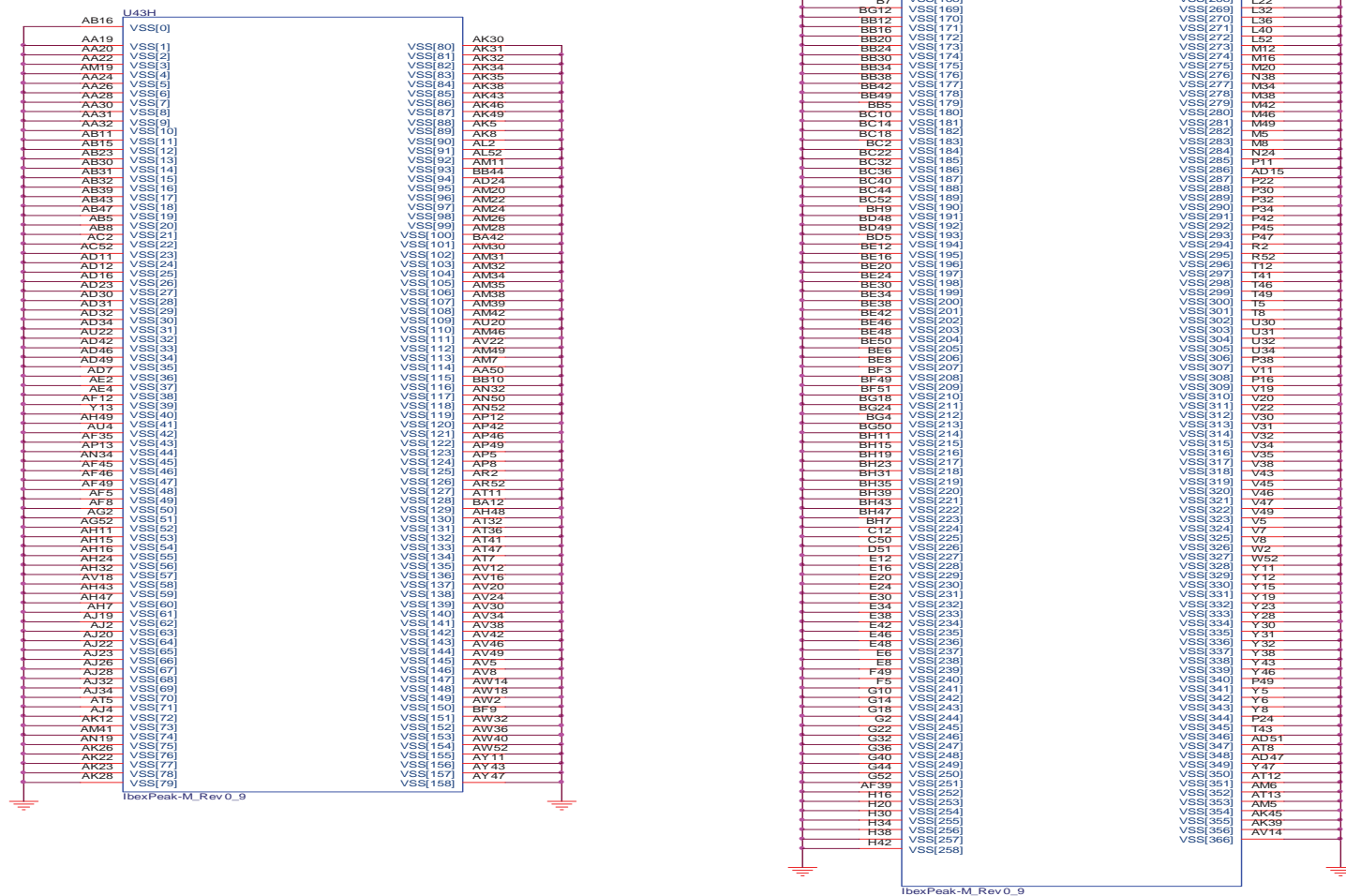
Sheet 26 of 49  
IBEXPEAK - M 8/9

## B.Schematic Diagrams

# IBEXPEAK - M 9/9

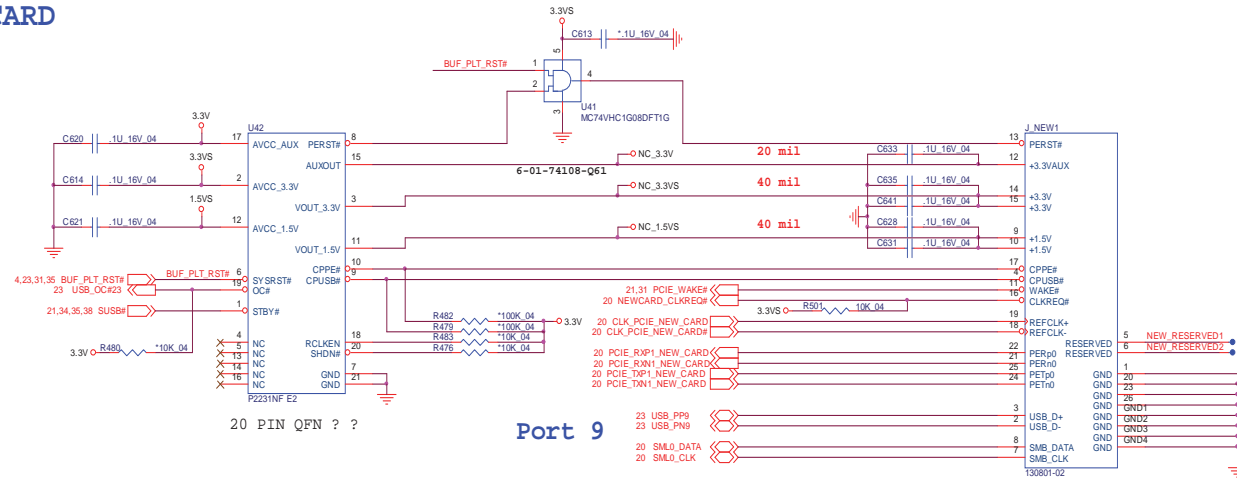
## IBEXPEAK - M (GND)

Sheet 27 of 49  
IBEXPEAK - M 9/9



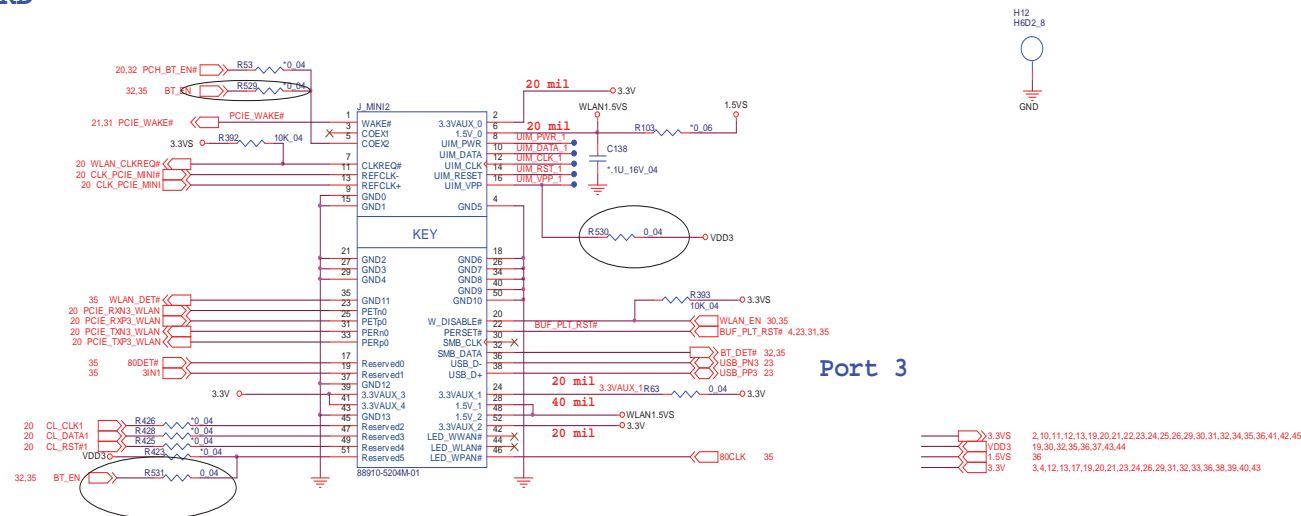
## New Card, Mini PCIE

## NEW CARD

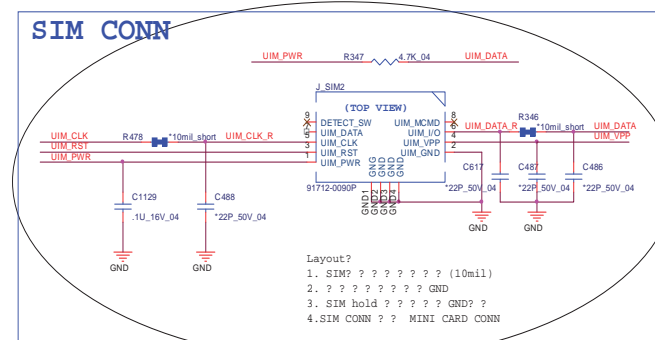
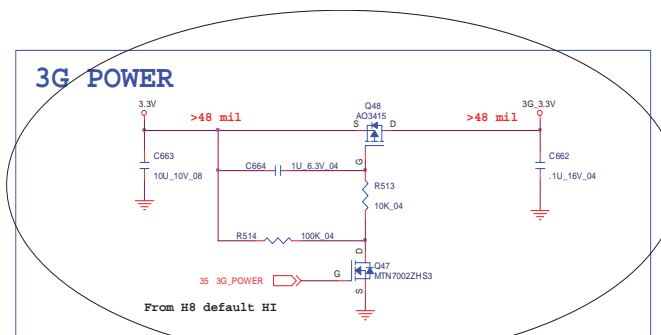
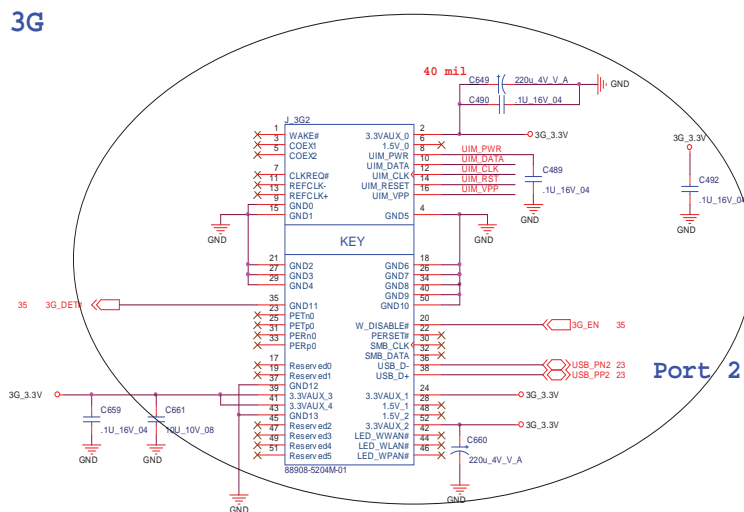


Sheet 28 of 49  
New Card, Mini PCIE

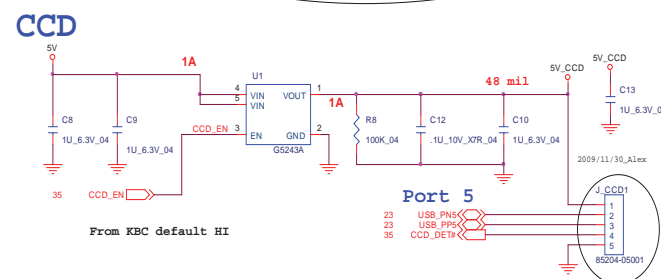
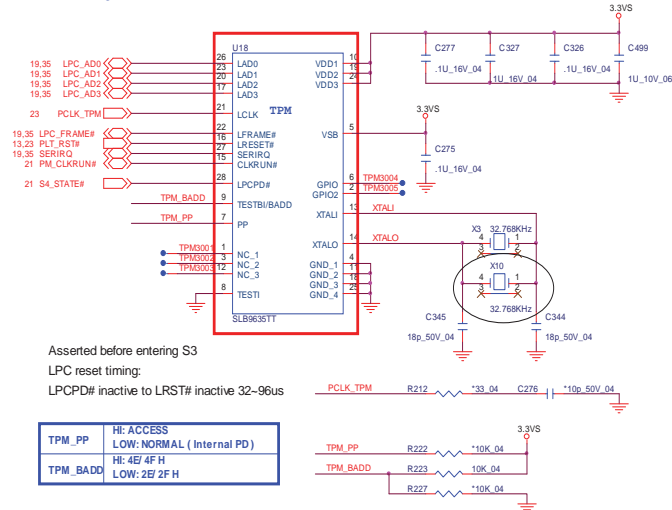
## MINI CARD



### 3G



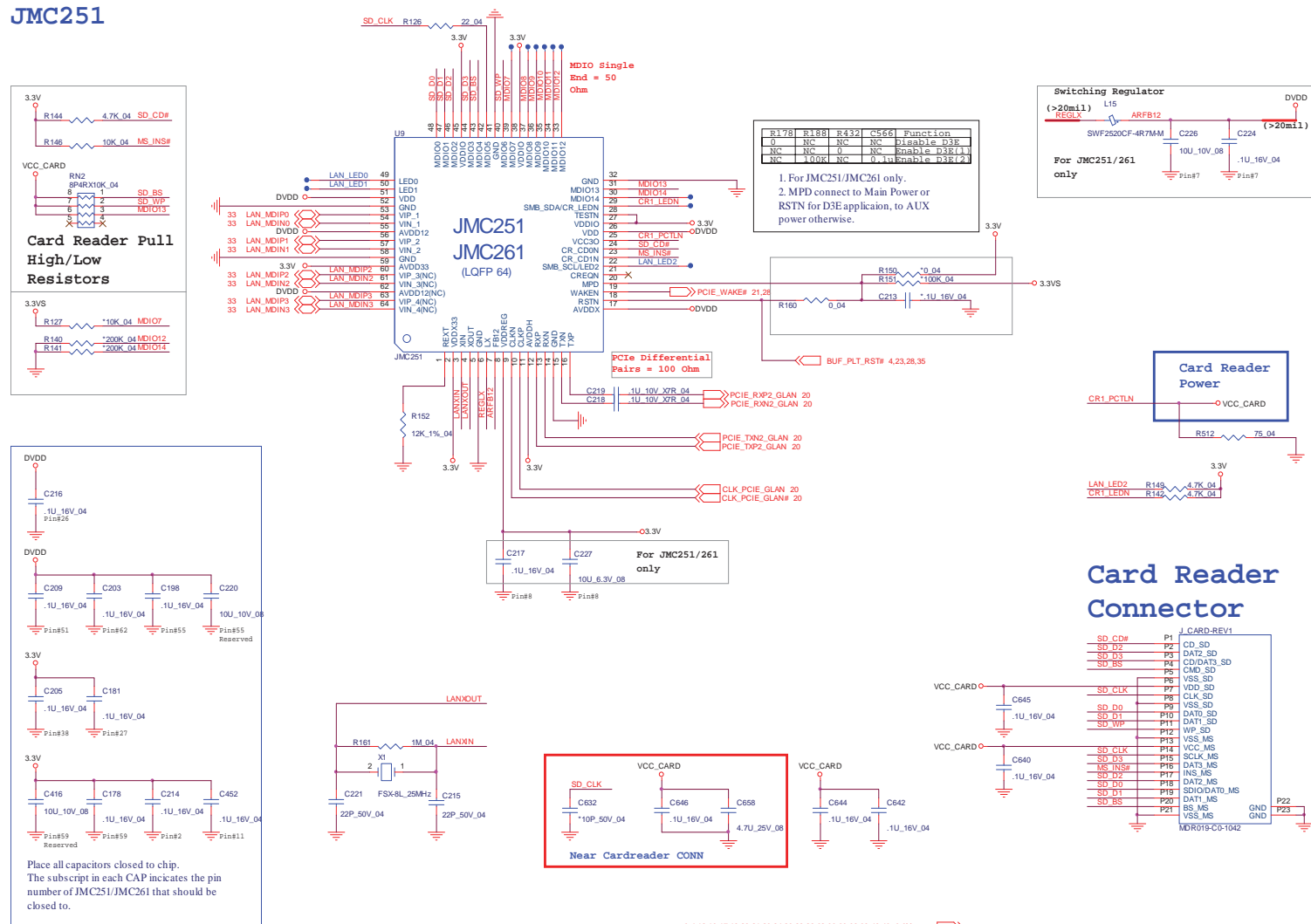
## TPM 1.2



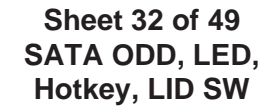
5V	26,30,34,36,38,39,40,43
3.3V	3,4,12,13,17,19,20,21,23,24,26,28,31,32,33,36,38,39,40,43
3.3VS	2,10,11,12,13,19,20,21,22,23,24,25,26,28,30,31,32,34,35,36,41,42,45



## JMC251



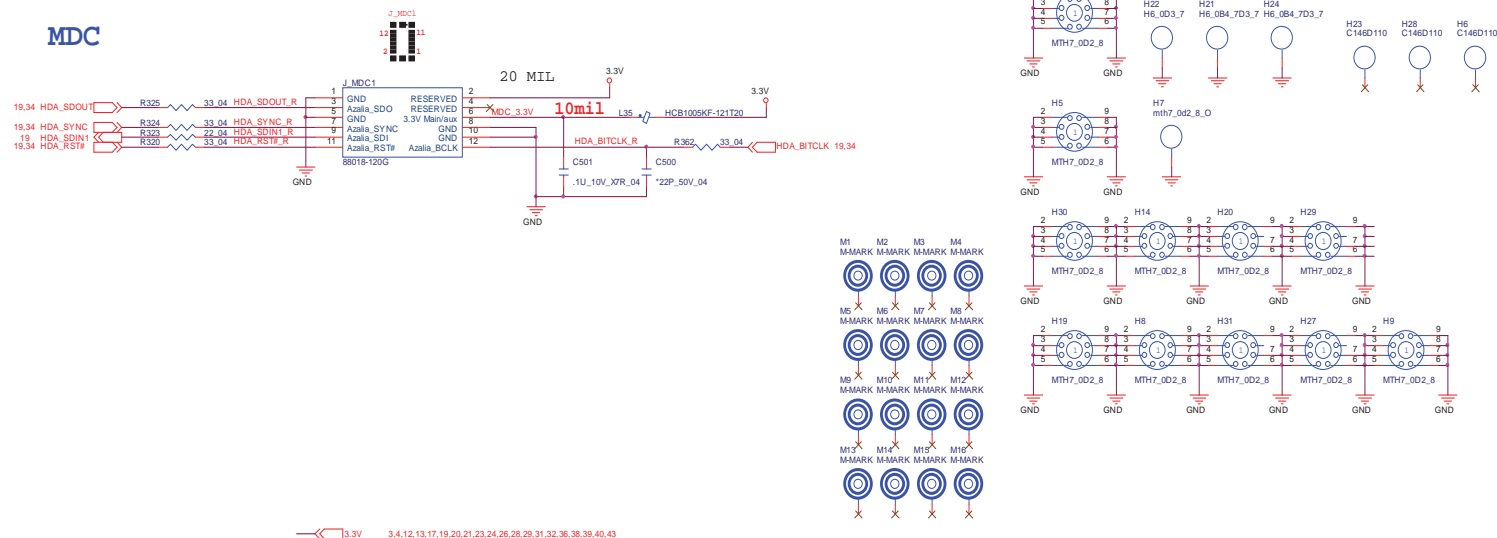
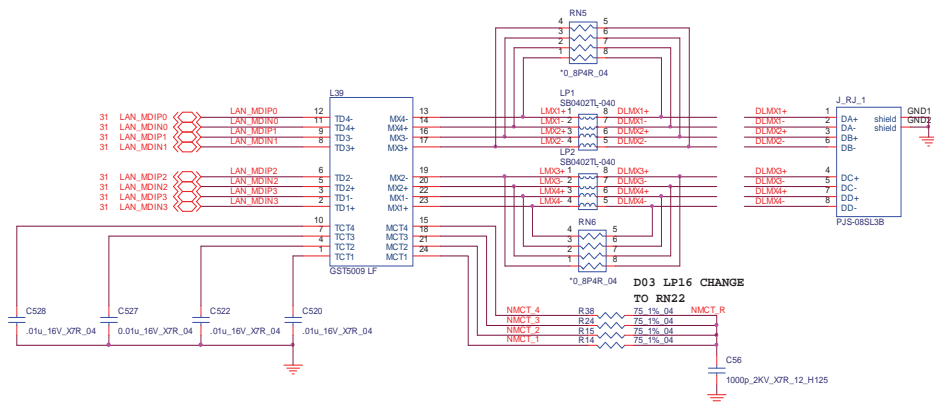
## B.Schematic Diagrams





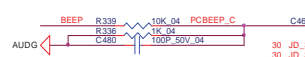
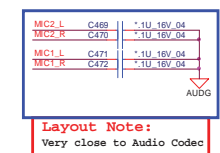
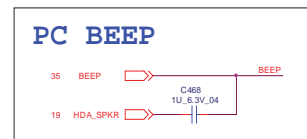
## B.Schematic Diagrams

**B - 34 RJ45, Modem**



# Audio Codec ALC272

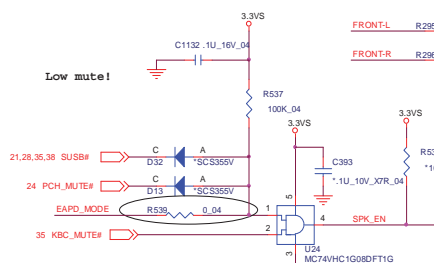
## CODEC ( ALC272-GR )



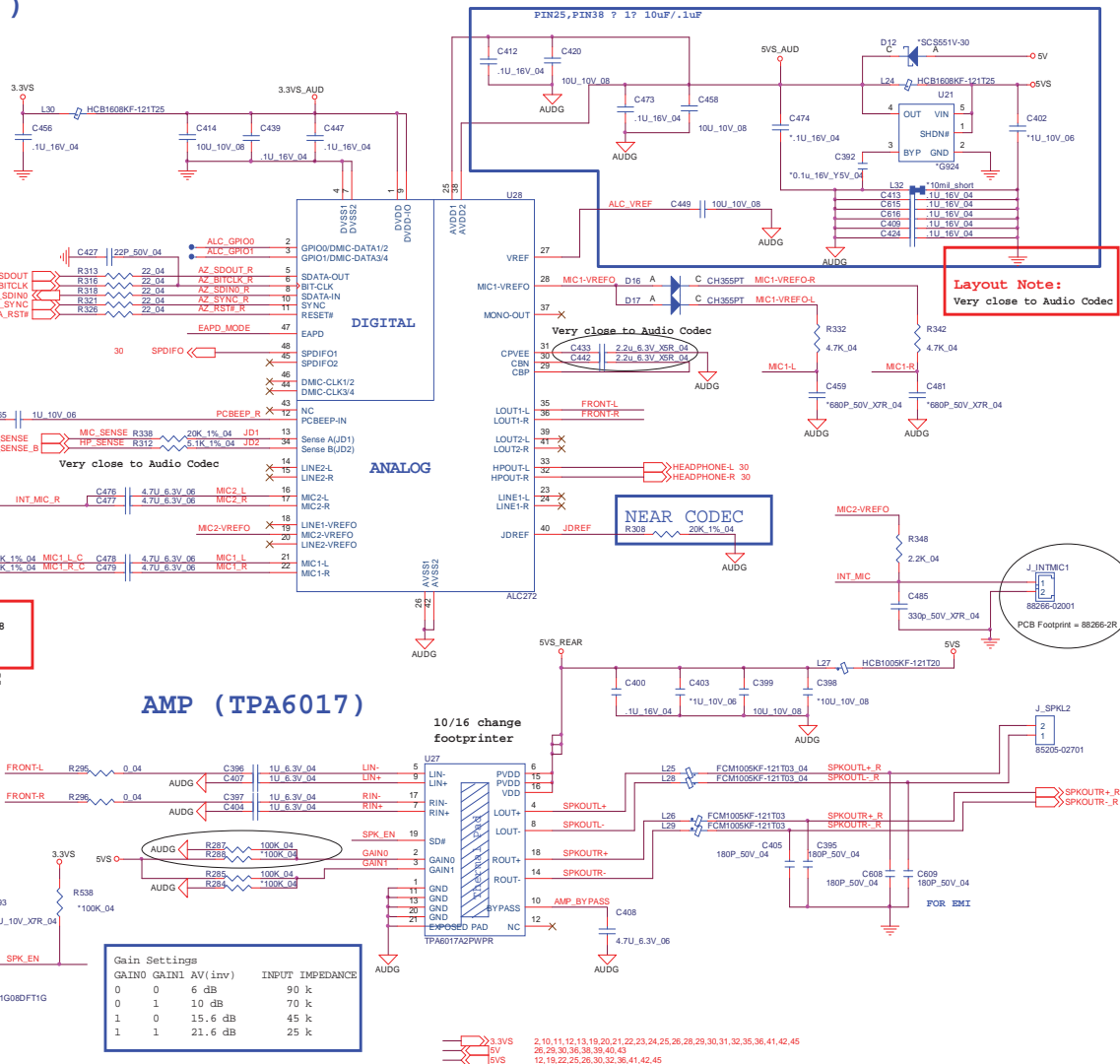
**Layout Note:**  
Codec pin 1 - pin 11 and pin 44 - pin 48 are Digital signals.  
The others are Analog signals.

PIN 13 ,PIN34 JD\_SENSE  
? ? ? ? ? ? ? ?

## AMP (TPA6017)



Gain	Gain1	AV(inv)	INPUT IMPEDANCE
0	0	6 dB	90 k
0	1	10 dB	70 k
1	0	15.6 dB	45 k
1	1	21.6 dB	25 k



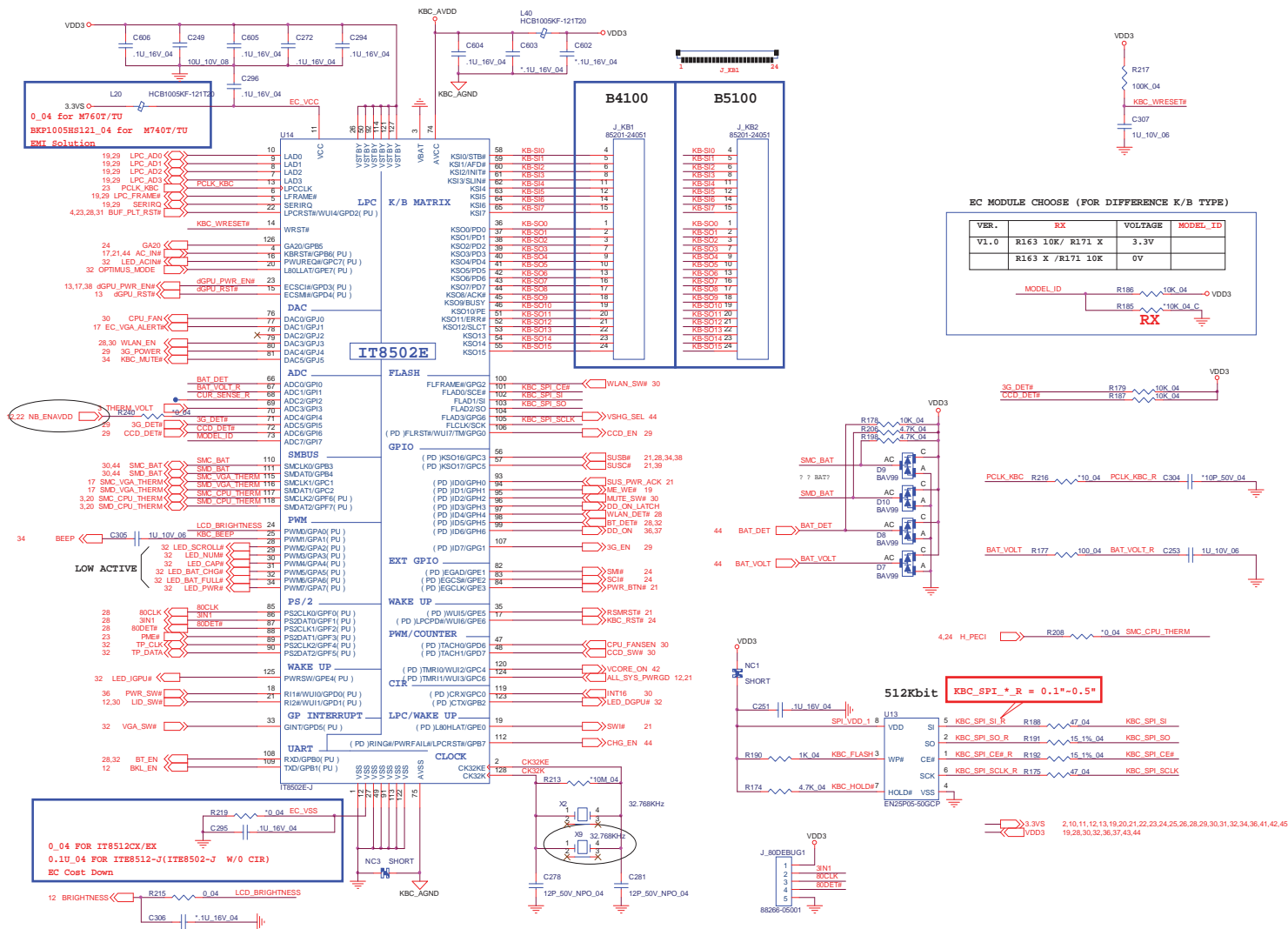
3.3V 2,10,11,12,13,18,20,21,22,23,24,25,26,28,29,30,31,32,35,36,41,42,45  
5V 26,28,30,36,38,39,40,43  
12,19,22,25,26,30,32,36,41,42,45

Sheet 34 of 49  
Audio Codec  
ALC272

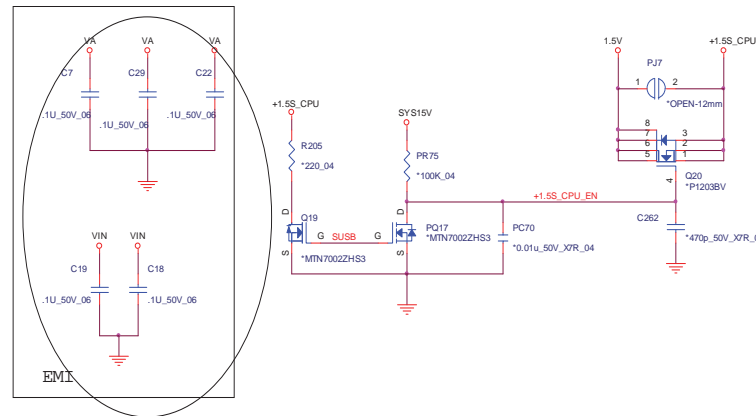
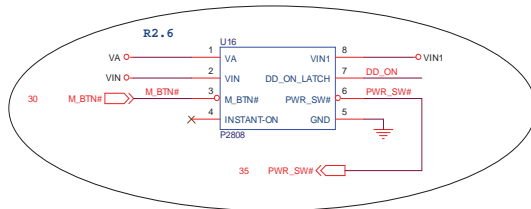
B.Schematic Diagrams

# KBC-ITE IT8502E

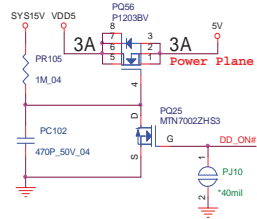
Sheet 35 of 49  
KBC-ITE IT8502E



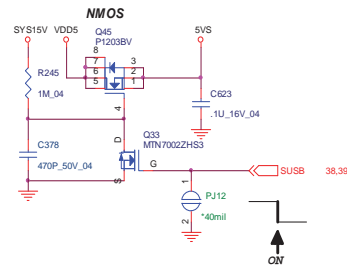
# 5VS, 3.3VS, 1.5VS, VIN1



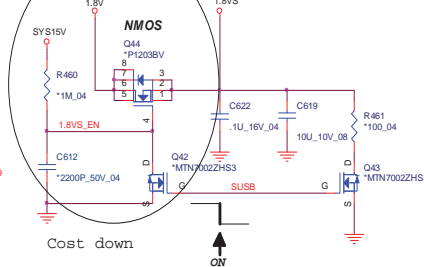
## 5V



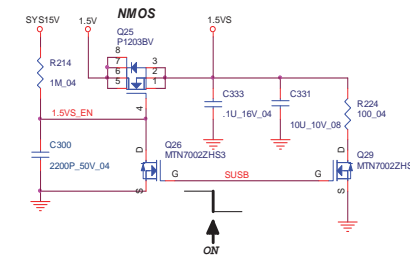
## 5VS



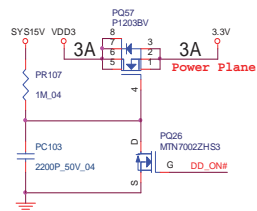
## 1.8VS



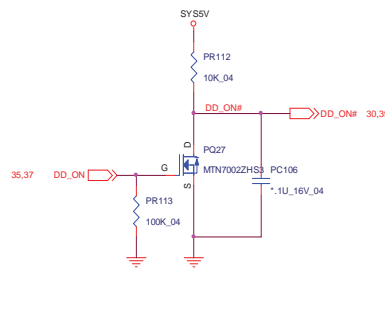
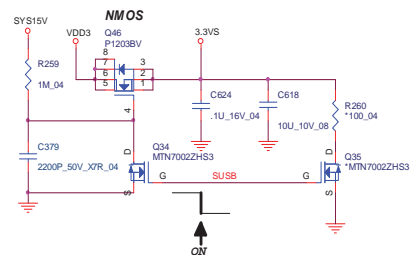
## 1.5VS



## 3.3V



## 3.3VS

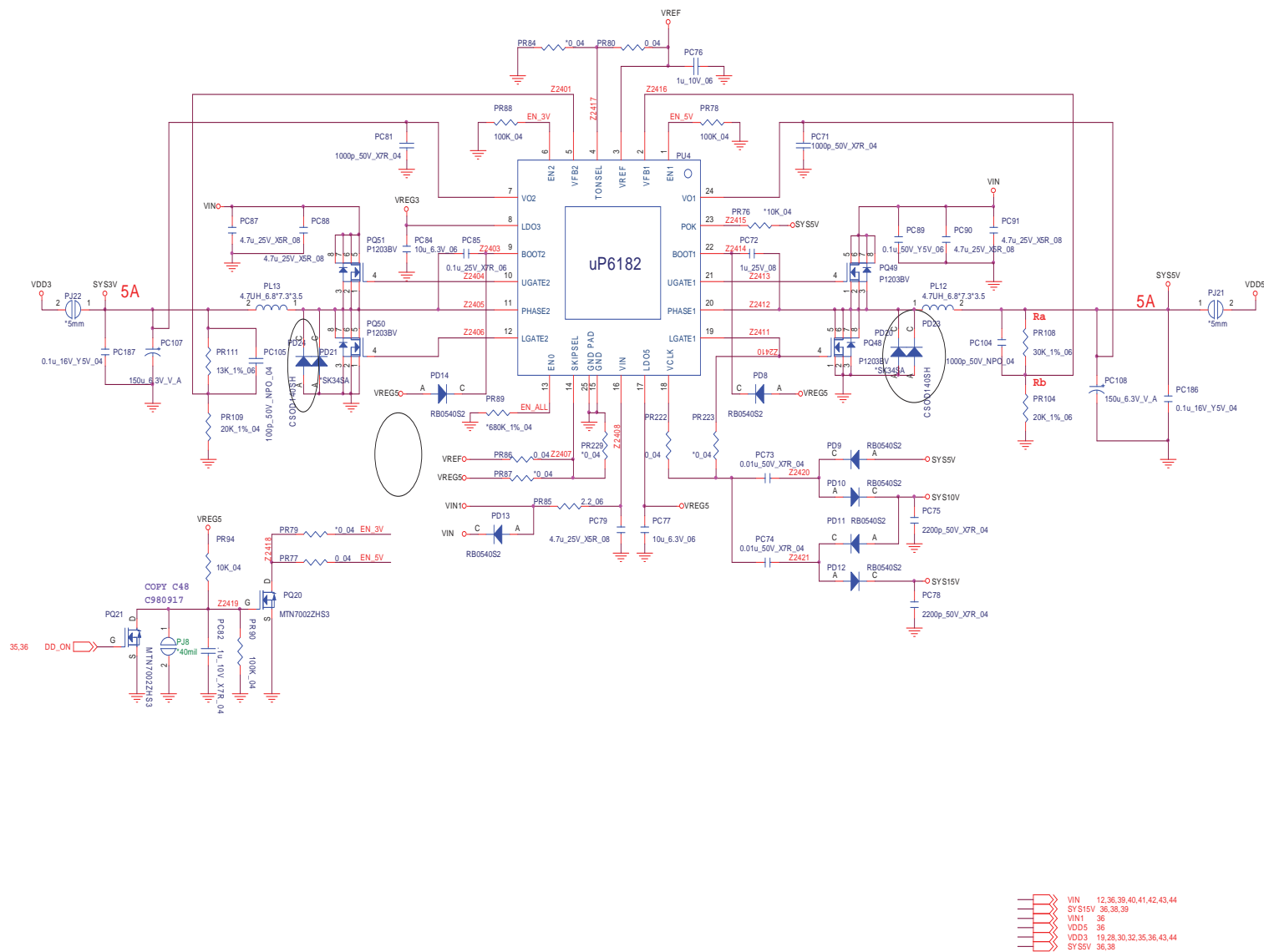


VIN1	37
VIN	12,37,39,40,41,42,43,44
VA	44
SYS15V	37,38,39
SYS5V	37,38
VDD5	37
5V	25,29,30,34,38,39,40,43
5VS	12,19,22,25,26,30,32,34,41,42,45
VDD3	19,26,30,32,35,37,43,44
3.3V	3,4,12,13,17,19,20,21,23,24,26,28,29,31,32,33,38,39,40,43
3.3VS	2,10,11,12,15,18,20,21,22,23,24,25,26,28,29,30,31,32,34,35,41,42,45
+1.5S_CPU	4,7
1.5V	4,10,11,39
1.5VS	28
1.8V	38
1.8VS	7,25,38

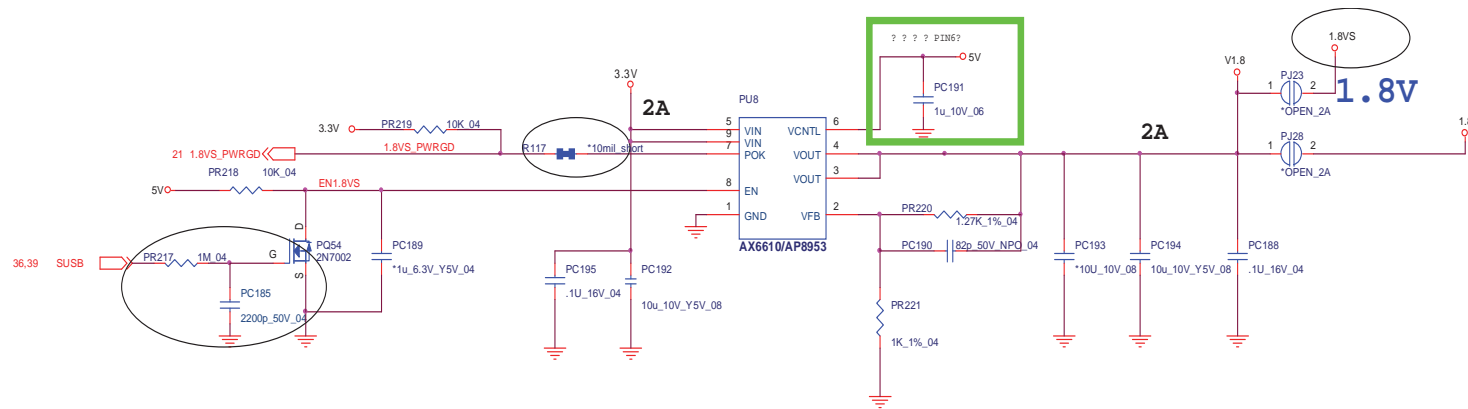
Sheet 36 of 49  
5VS, 3.3VS, 1.5VS,  
VIN1

# VDD3, VDD5

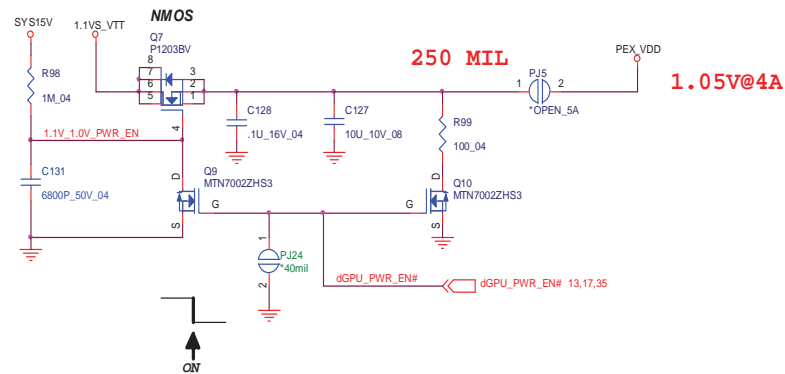
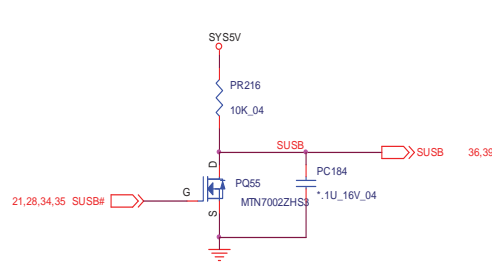
Sheet 37 of 49  
VDD3, VDD5



## Power 1.8V, PEX\_VDD

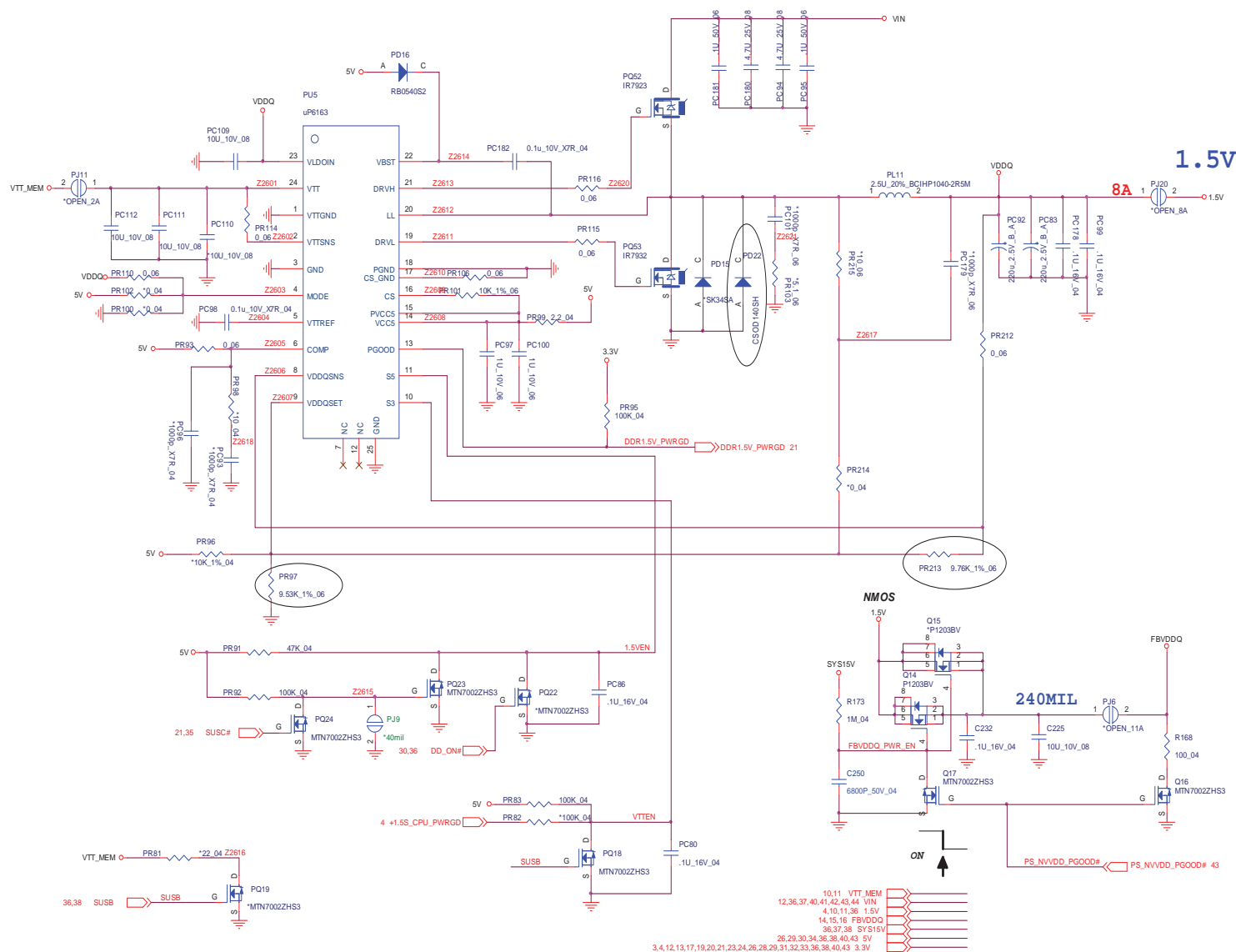


Sheet 38 of 49  
Power 1.8V,  
PEX\_VDD

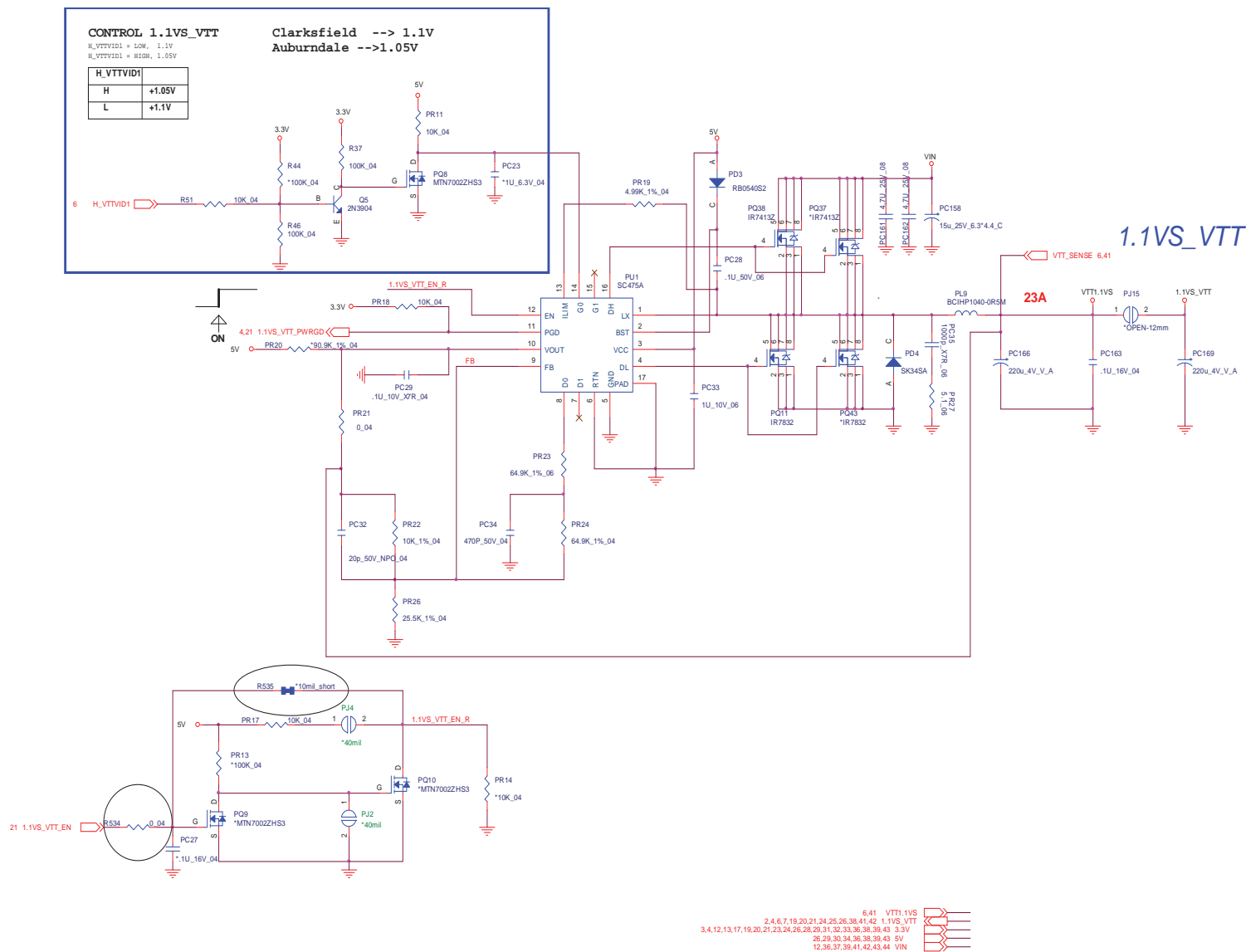


3,4,12,13,17,19,20,21,23,24,26,28,29,31,32,33,36,39,40,43 3.3V  
36,37,39 SYS15V  
36,37 SYS5V  
12,36,37,39,40,41,42,43,44 VIN  
13,14 PEX\_VDD  
2,4,6,7,19,20,21,24,25,26,40,41,42 1.1VS\_VTT  
26,29,30,34,36,39,40,43 5V  
36 1.8V  
7,25,36 1.8VS

## B.Schematic Diagrams



# Power 1.1VS\_VTT

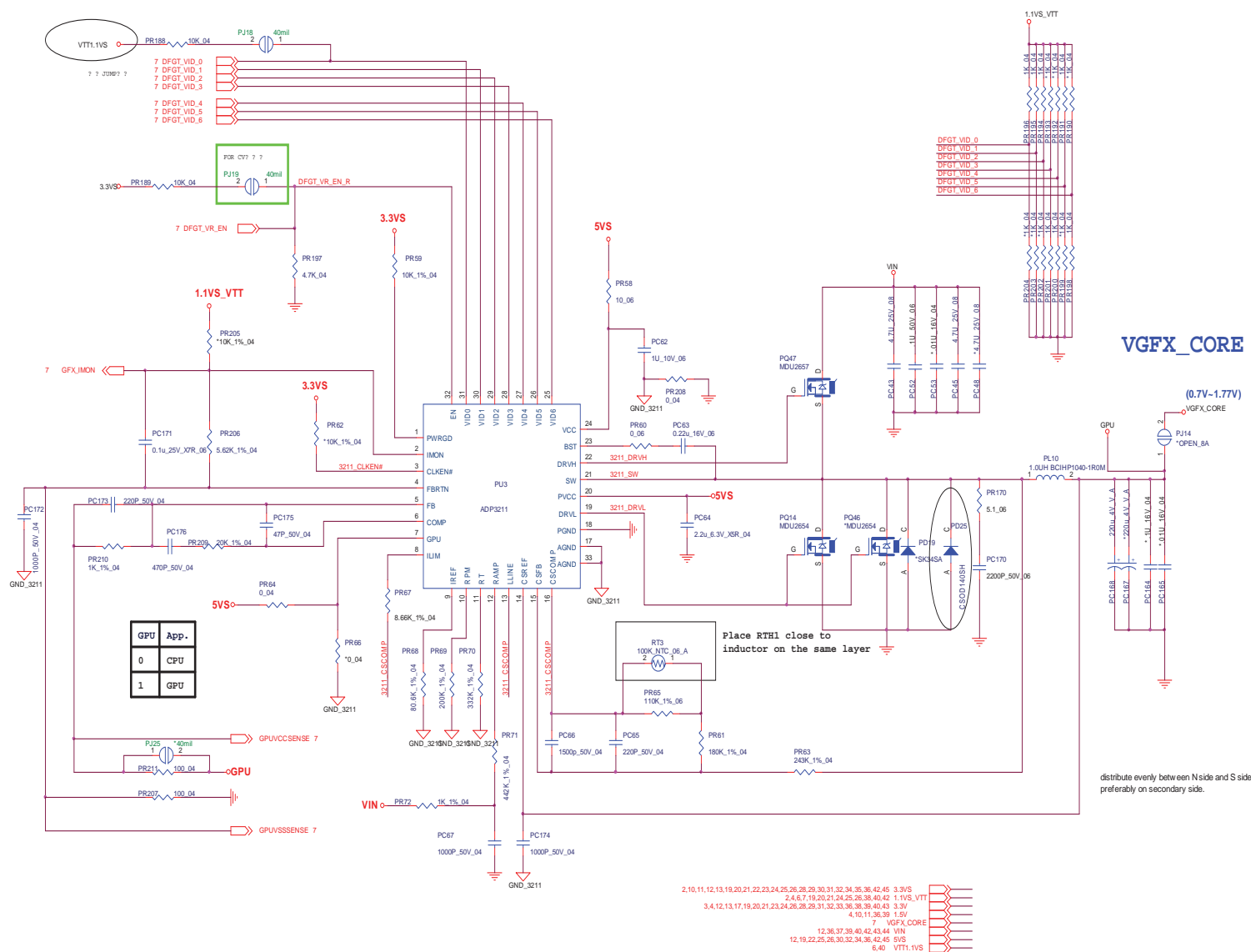


Sheet 40 of 49  
Power 1.1VS\_VTT

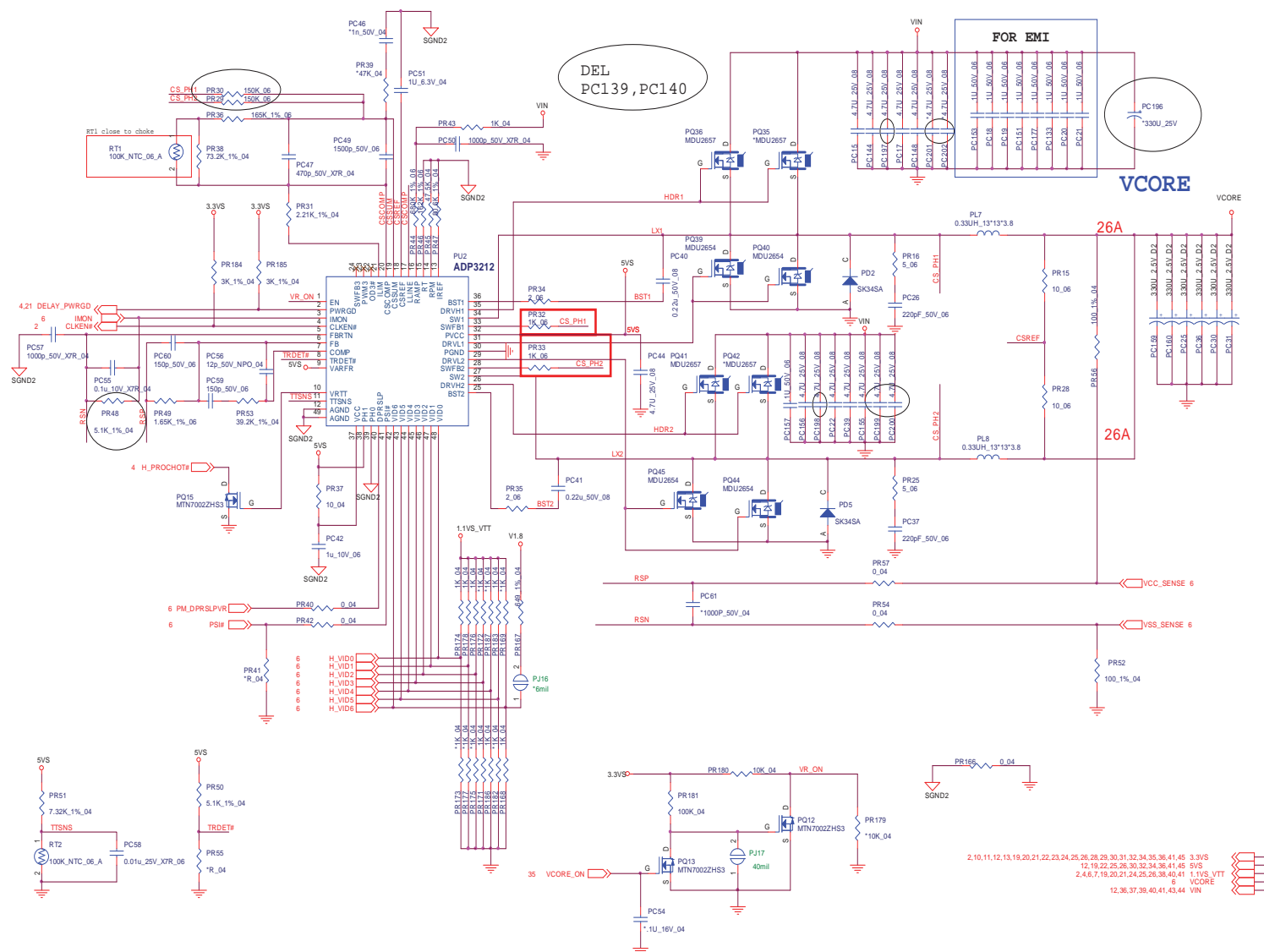


## B.Schematic Diagrams

VGFX\_CORE



# V-Core



Sheet 42 of 49  
V-Core

## NVIDIA N11P-GE1

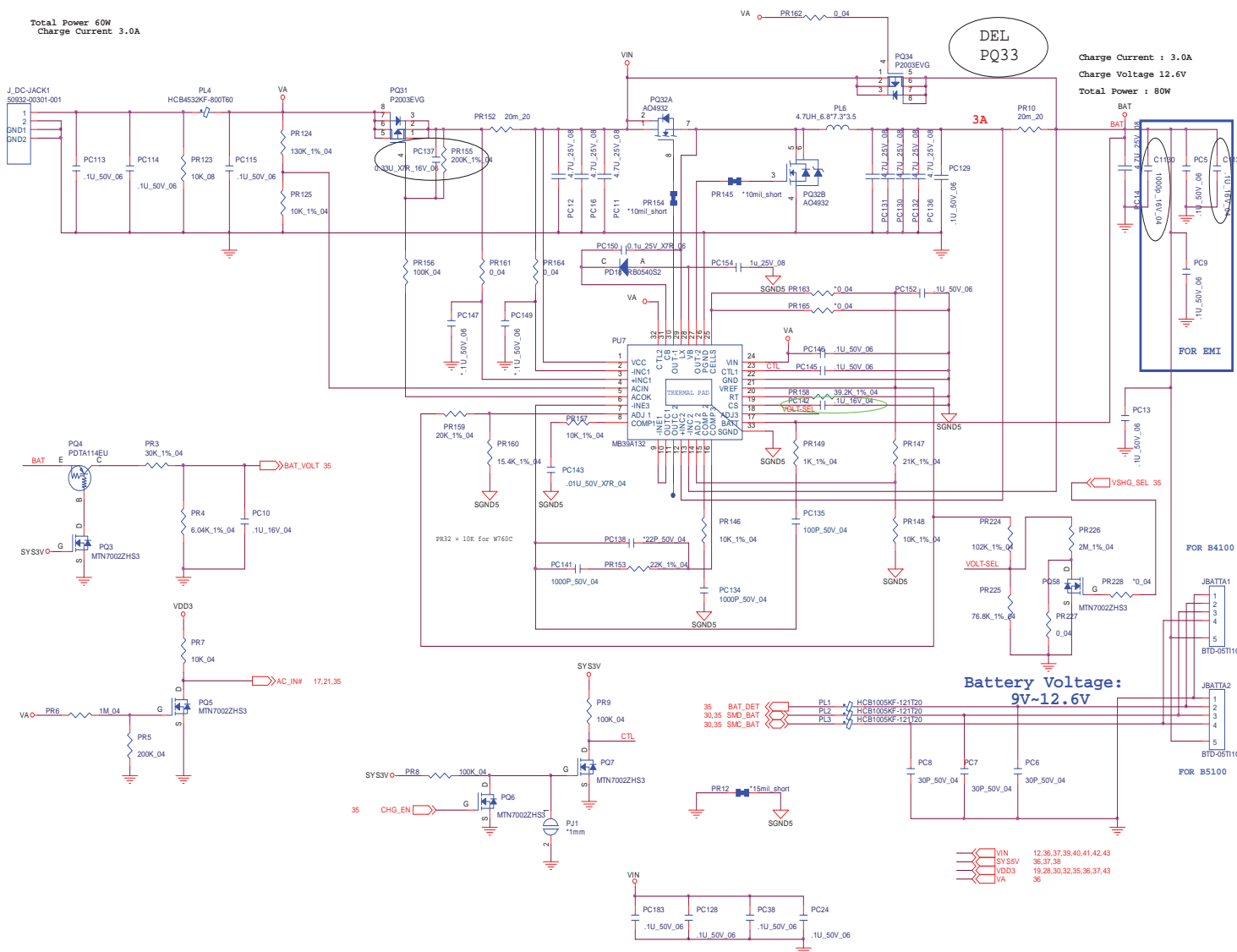
	PR131	PR132	PR139	PR142	PR143	PR144
B4100 N11M-OP1	15K_1%	75K_1%	6.8K_1%	20K_1%	100K_1%	10K_1%
B5100 N11P-GE1	10K_1%	20K_1%	2K_1%	4.99K_1%	30K_1%	10K_1%

	1.03V	0.95V	0.85V	0.80V
GPIO5_NVVDD_VID0	0	1	0	1
GPIO6_NVVDD_VID1	0	0	1	1

## B.Schematic Diagrams

**Sheet 43 of 49**  
**Power VGA NVVDD**

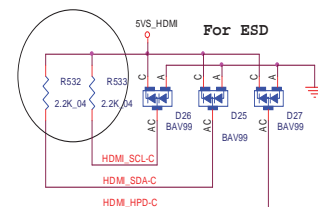
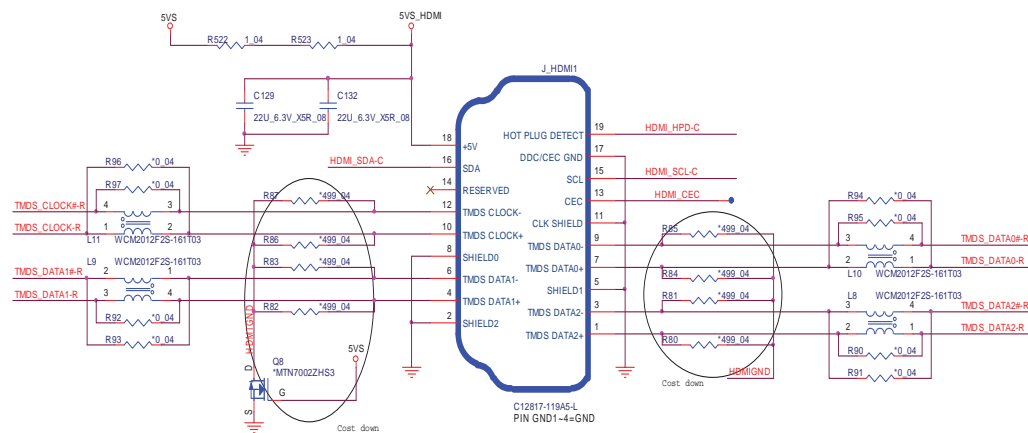
# AC\_IN, Charger



Sheet 44 of 49  
AC\_IN, Charger

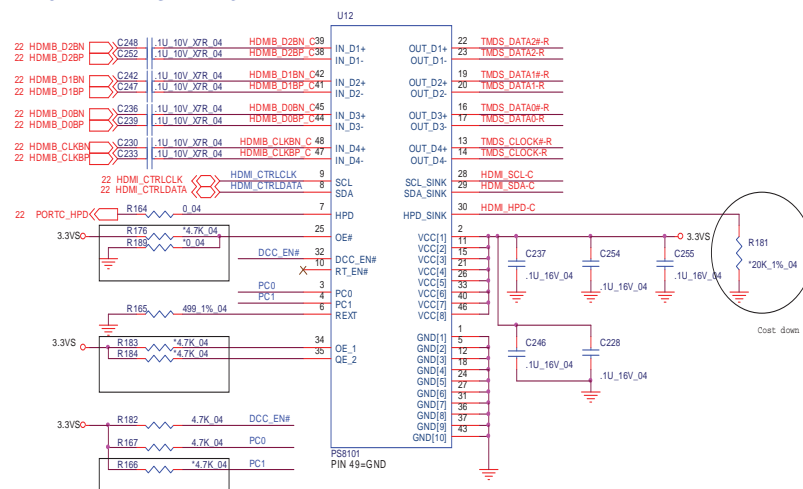
## HDMI CONNECTOR


2009/11/28\_Alex




**Sheet 45 of 49**  
**HDMI**

## B.Schematic Diagrams



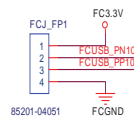
—  3.3VS 2,10,11,12,13,19,20,21,22,23,24,25,26,28,29,30,31,32,34,35,36,41,42

—  5VS 12,19,22,25,26,30,32,34,36,41,42

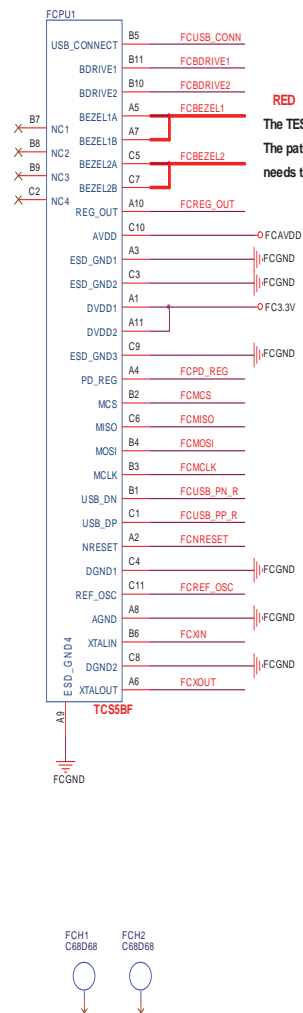


## Schematic Diagrams

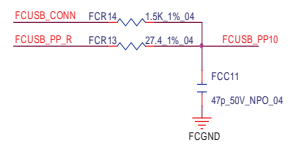
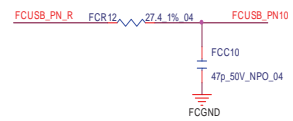
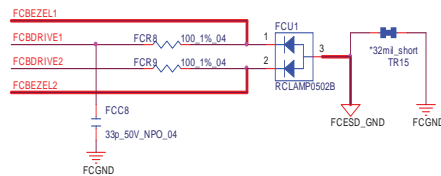
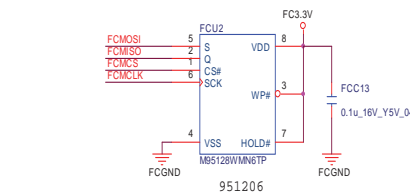
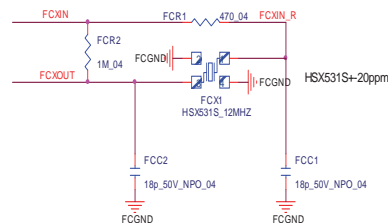
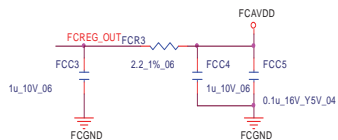
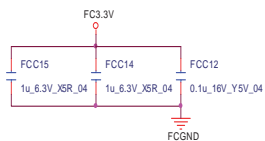
# B4100 Fingerprint Board



**B4100M ONLY**



**RED**  
The TESD\_GND trace has to be wide (> 20mil)  
The path be marked in  
needs to be design to be short and at low impedance.

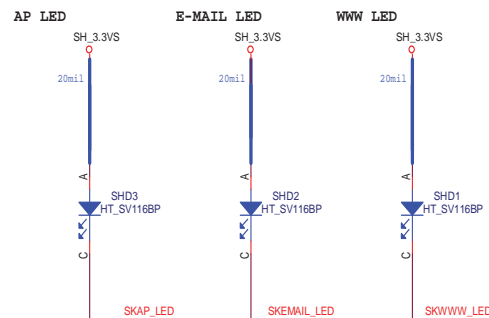
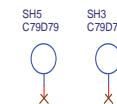
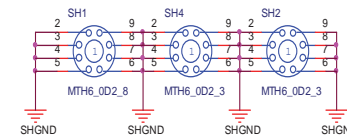
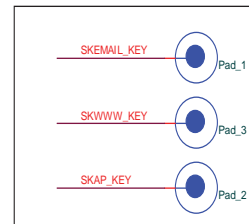
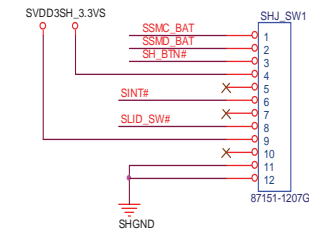
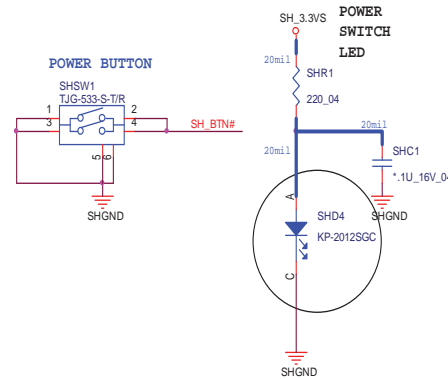
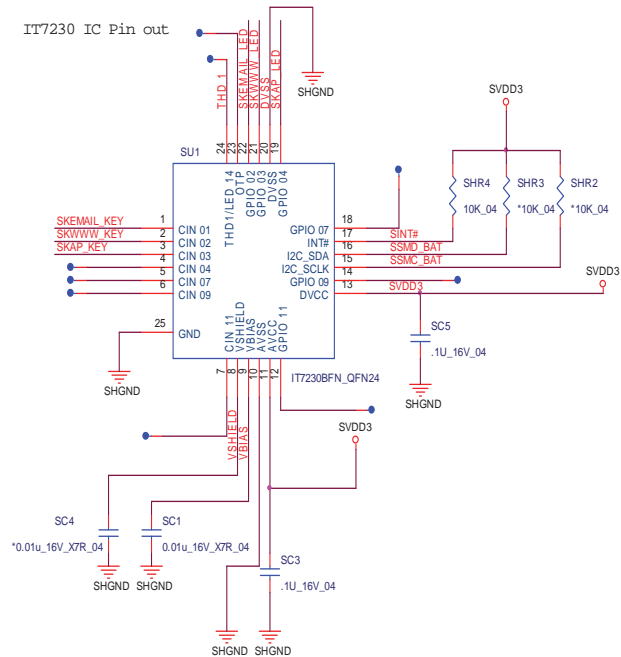


B.Schematic Diagrams

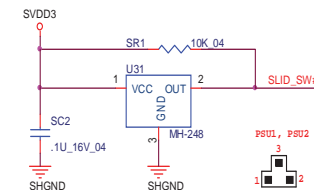
Sheet 47 of 49  
B4100 Fingerprint  
Board

# B4100 Power Switch Board

## POWER SW & POWER LED



B4100M ONLY



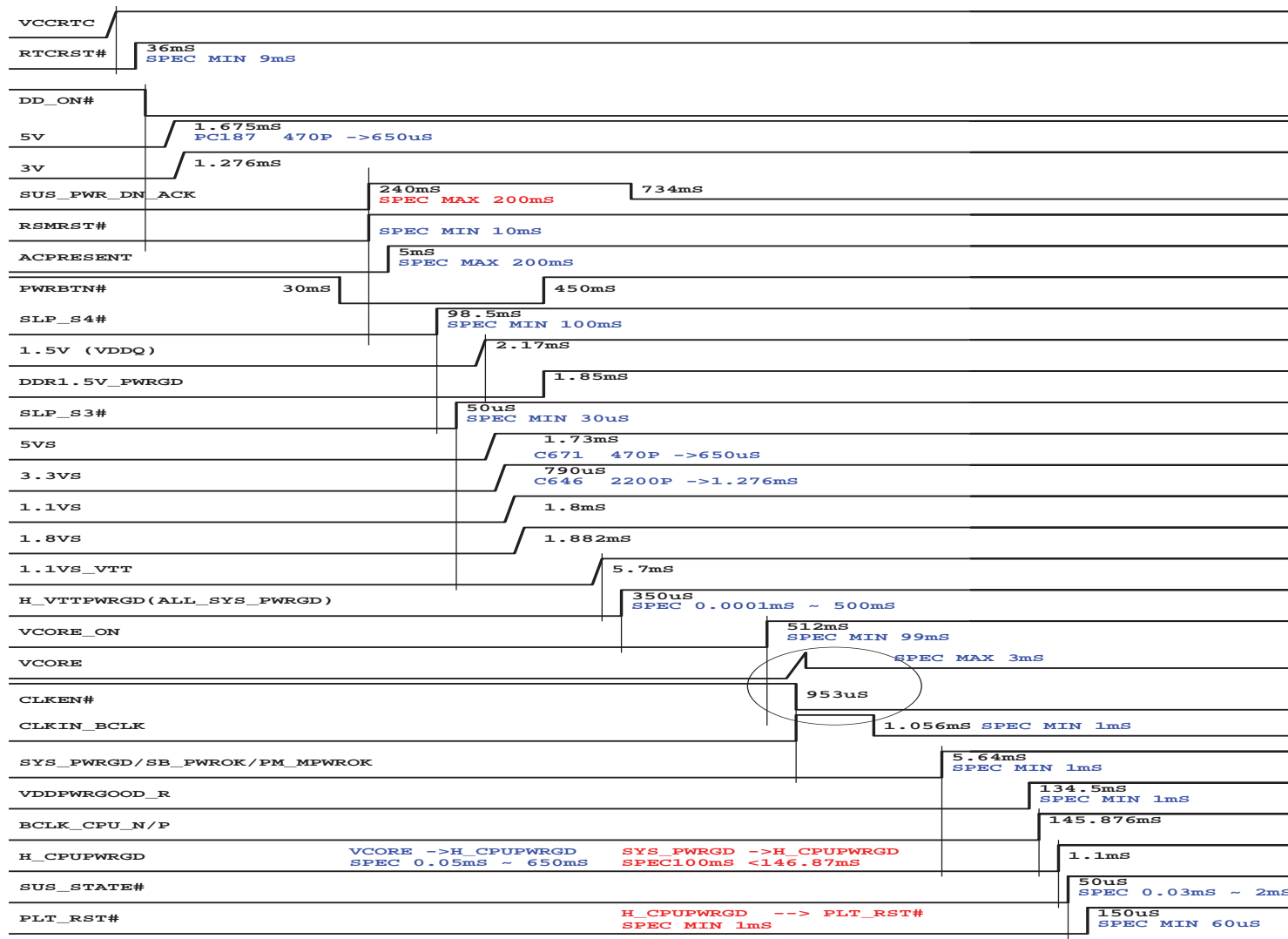
LID SWITCH IC

Sheet 48 of 49  
B4100 Power  
Switch Board



## Sequence

B 4 1 0 0 D 0 1 P O W E R S E Q U E N C E



Sheet 49 of 49  
Sequence

# Appendix C: Updating the FLASH ROM BIOS

## To update the FLASH ROM BIOS you must:

- Download the BIOS update from the web site.
- Unzip the files onto a bootable CD/DVD/USB Flash Drive.
- Reboot your computer from an external CD/DVD/USB Flash Drive.
- Use the flash tools to update the flash BIOS using the commands indicated below.
- Restart the computer booting from the HDD and press **F2** at startup enter the BIOS.
- Load setup defaults from the BIOS and save the default settings and exit the BIOS to restart the computer.
- After rebooting the computer you may restart the computer again and make any required changes to the default BIOS settings.

## Download the BIOS

1. Go to [www.clevo.com.tw](http://www.clevo.com.tw) and point to **E-Services** and click **E-Channel**.
2. Use your user ID and password to access the appropriate download area (BIOS), and download the latest BIOS files (the BIOS file will be contained in a batch file that may be run directly once unzipped) for your computer model (see sidebar for important information on BIOS versions).

## Unzip the downloaded files to a bootable CD/DVD/ or USB Flash drive

1. Insert a bootable CD/DVD/USB flash drive into the CD/DVD drive/USB port of the computer containing the downloaded files.
2. Use a tool such as Winzip or Winrar to unzip all the BIOS files and refresh tools to your bootable CD/DVD/USB flash drive (you may need to create a bootable CD/DVD with the files using a 3rd party software).

## Set the computer to boot from the external drive

1. With the bootable CD/DVD/USB flash drive containing the BIOS files in your CD/DVD drive/USB port, restart the computer and press **F2** (in most cases) to enter the BIOS.
2. Use the arrow keys to highlight the **Boot** menu.
3. Use the “+” and “-” keys to move boot devices up and down the priority order.
4. Make sure that the CD/DVD drive/USB flash drive is set first in the boot priority of the BIOS.
5. Press **F10** to save any changes you have made and exit the BIOS to restart the computer.



### BIOS Version

Make sure you download the latest correct version of the BIOS appropriate for the computer model you are working on.

**You should only download BIOS versions that are V1.01.XX or higher as appropriate for your computer model.**

Note that BIOS versions are not backward compatible and therefore **you may not downgrade your BIOS to an older version** after upgrading to a later version (e.g if you upgrade a BIOS to ver 1.01.05, you **MAY NOT** then go back and flash the BIOS to ver 1.01.04).

## BIOS Update

---

### Use the flash tools to update the BIOS

1. Make sure you are not loading any memory management programs such as HIMEM by holding the **F8** key as you see the message “**Starting MS-DOS**”. You will then be prompted to give “**Y**” or “**N**” responses to the programs being loaded by DOS. Choose “**N**” for any memory management programs.
2. You should now be at the DOS prompt e.g: `DISK C:\>` (C is the designated drive letter for the CD/DVD drive/USB flash drive).
3. **Type the following command** at the DOS prompt:

**C:\> Flash.bat**

4. The utility will then proceed to flash the BIOS.
5. You should then be prompted to press any key to restart the system or turn the power off, and then on again but make sure you remove the CD/DVD/USB flash drive from the CD/DVD drive/USB port before the computer restarts.

### Restart the computer (booting from the HDD)

1. With the CD/DVD/USB flash drive removed from the CD/DVD drive/USB port the computer should restart from the HDD.
2. Press **F2** as the computer restarts to enter the BIOS.
3. Use the arrow keys to highlight the **Exit** menu.
4. Select **Load Setup Defaults** (or press **F9**) and select “**Yes**” to confirm the selection.
5. Press **F10** to save any changes you have made and exit the BIOS to restart the computer.

### Your computer is now running normally with the updated BIOS

You may now enter the BIOS and make any changes you require to the default settings.